

MÖBELIX

A diagram of a 32-bit register. It consists of 32 flip-flops arranged in a single row. Each flip-flop has a data input on the left, a clock input on the top, and a data output on the right. The clock inputs of all flip-flops are connected to a common clock line. The data outputs of all flip-flops are connected to a common data bus. A hand is shown pointing to the clock input of one of the flip-flops.

OFF

ON

8 A

81810125-01
81810125-02
81810125-03

spare parts		
code	part	
A		x 5 PCS
B		x 5 PCS