

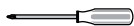
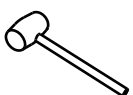
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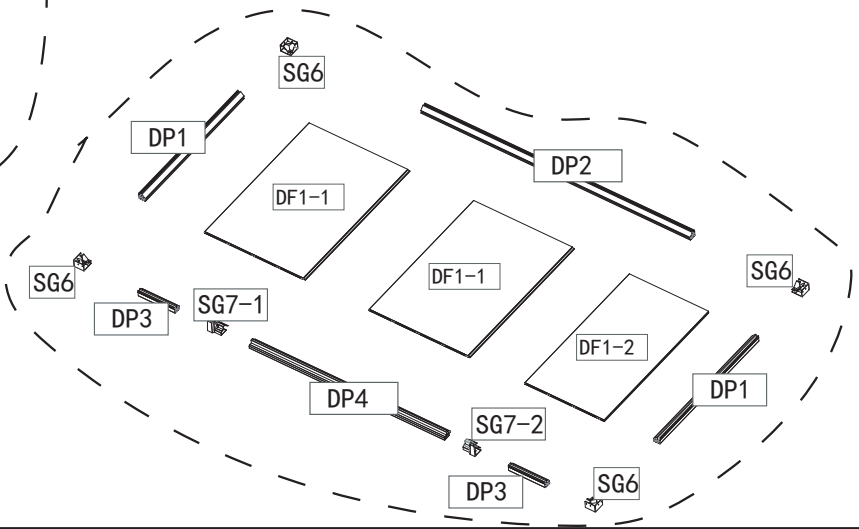
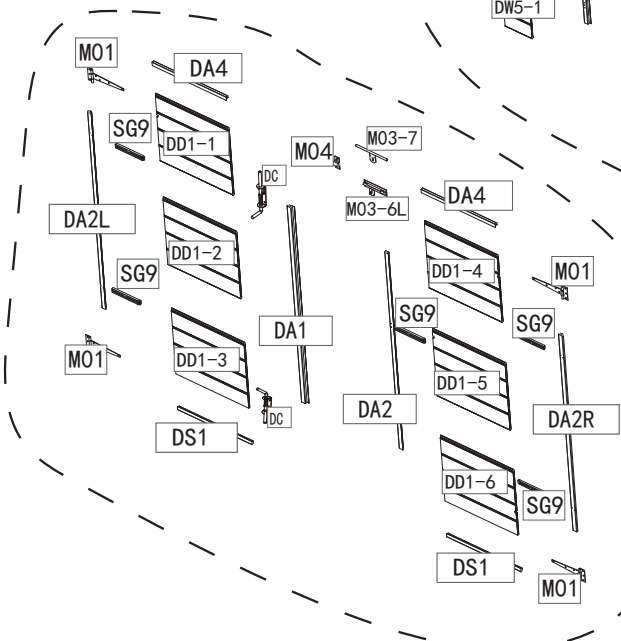
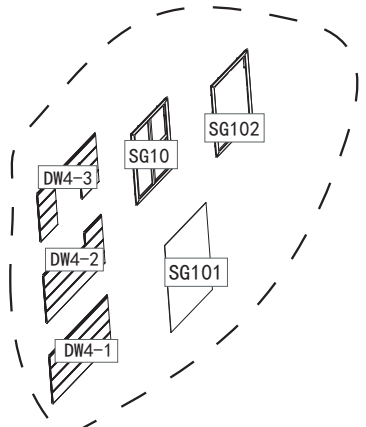
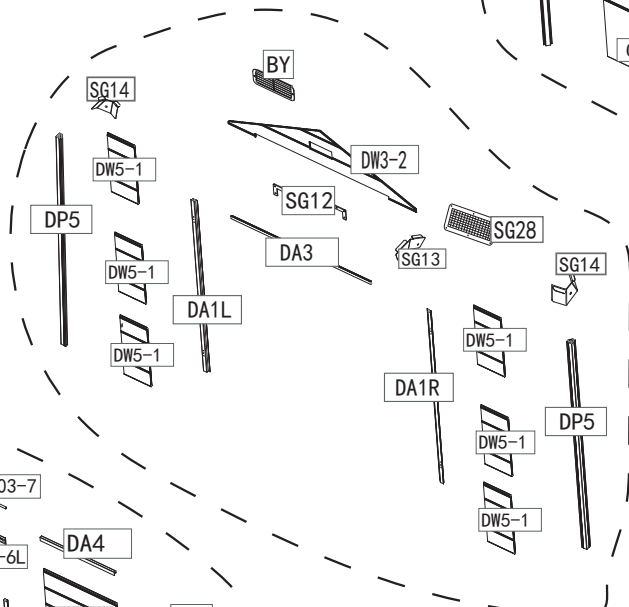
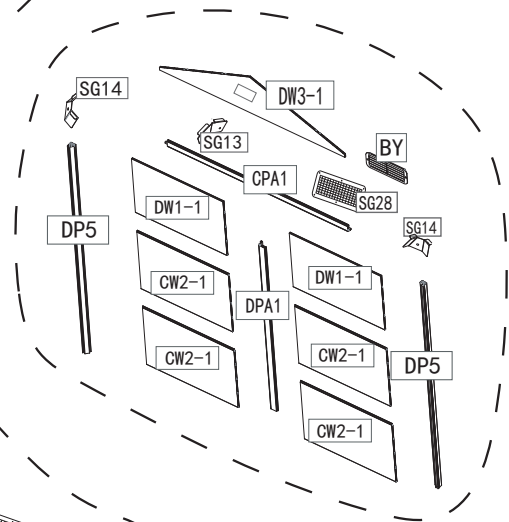
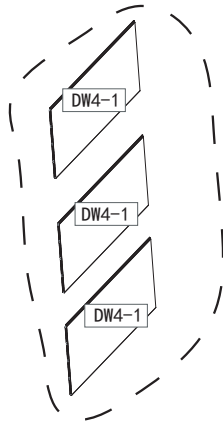
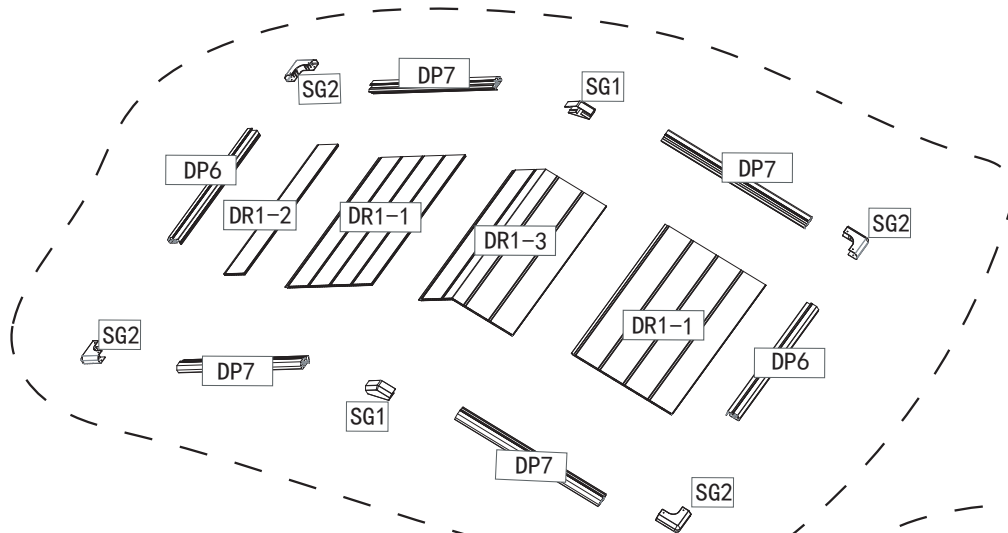
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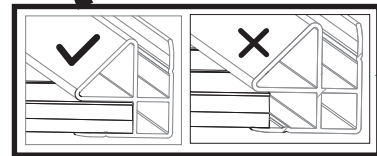
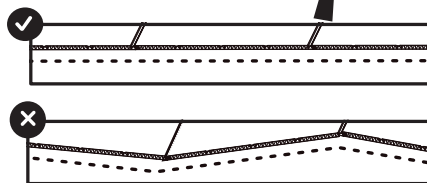
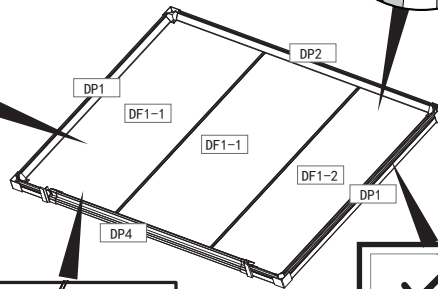
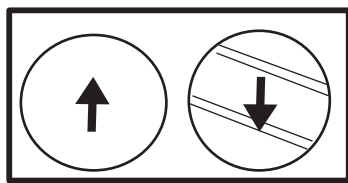
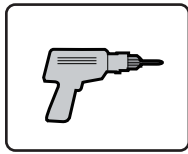
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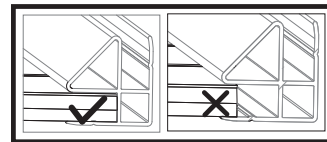
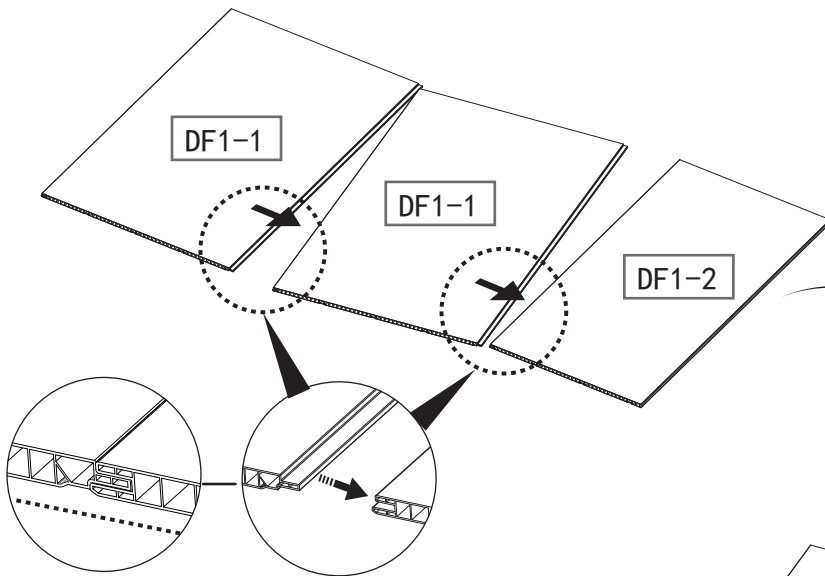
NO.	PART.	Qty.
DF1-1		2
DR1-1		2
DR1-3		1
DW1-1		2
CW2-1		4
DW5-1		6
DW4-1		4
DW4-3		1
DW4-2		1
DD1-2		1
DD1-1		1
DD1-3		1
DD1-4		1
DD1-5		1
DD1-6		1
DP2		1
DP1		2
DP3		2
DP5		4
DP4		1
DP6		2
DP7		4
DA1		1
DA1R		1
DA1L		1
DA3		1
DA2		1
DA2R		1
DA2L		1
DS1		2
DA4		2

NO.	PART.	Qty.
DPA1		1
CPA1		1
SG10		1
SG101		1
SG102		1
DR1-2		1
DW3-2		1
DW3-1		1
DF1-2		1
SG13		2
SG14		4
SG6		4
SG12		2
Drill		1
DC		2
SG9		5
SG1		2
BY		2
M01		4
M03-6L		1
M03-7		1
M04		1
SG2		4
SG7-1		1
SG7-2		1
SG8		2
ZW413		22
Z410		96
Z420		105
Z425		4
SG28		2





Step 1



DF1-1

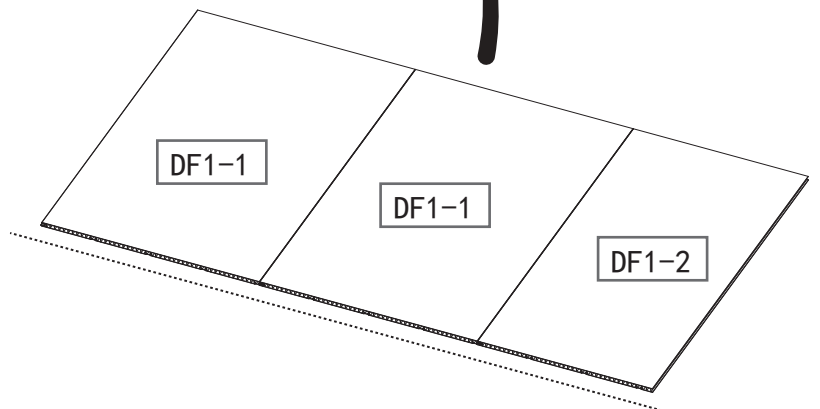
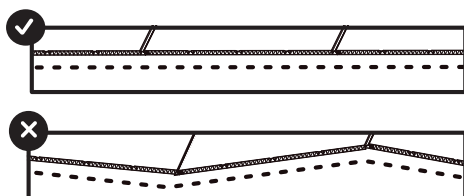


2 PC

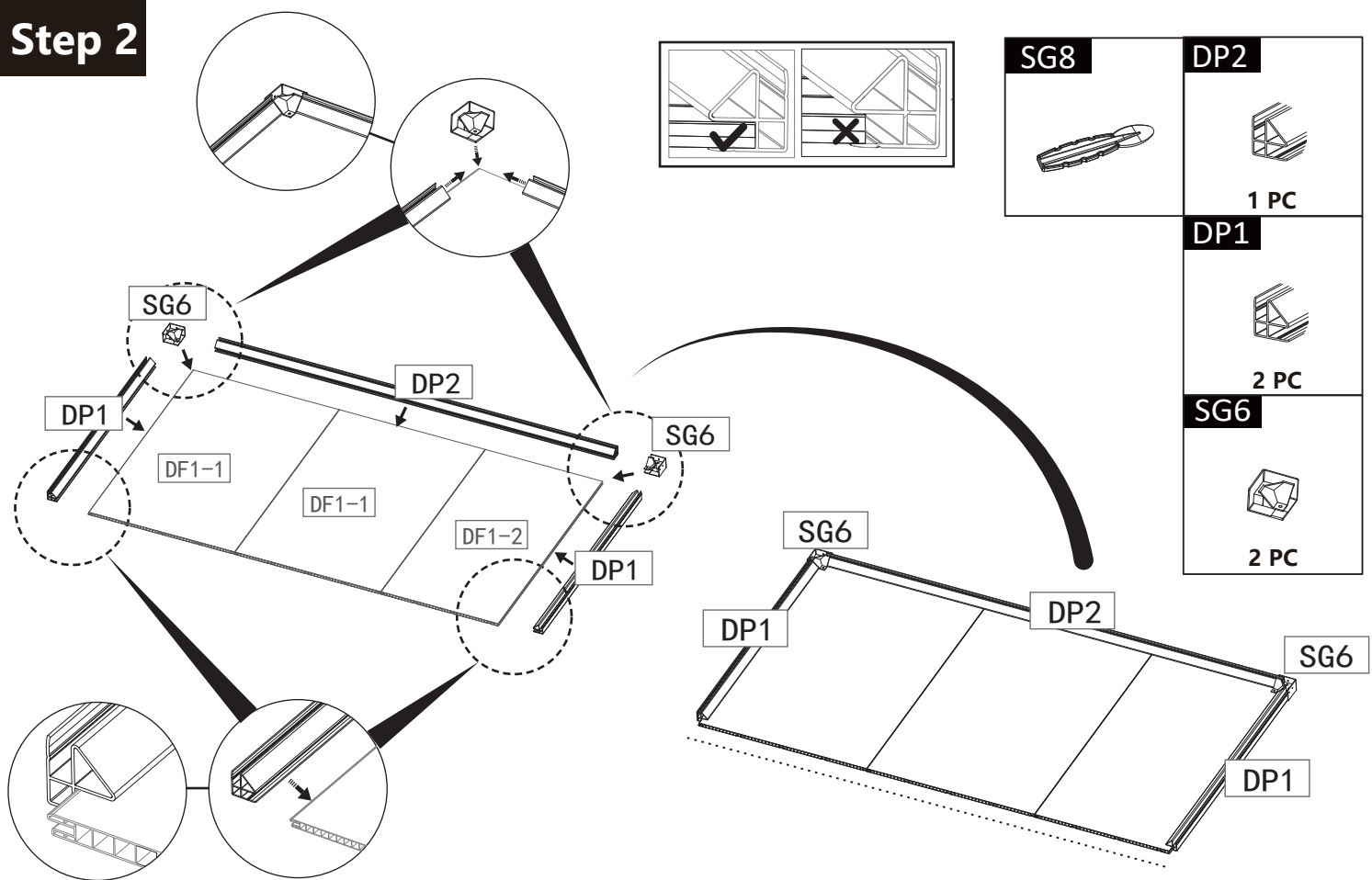
DF1-2



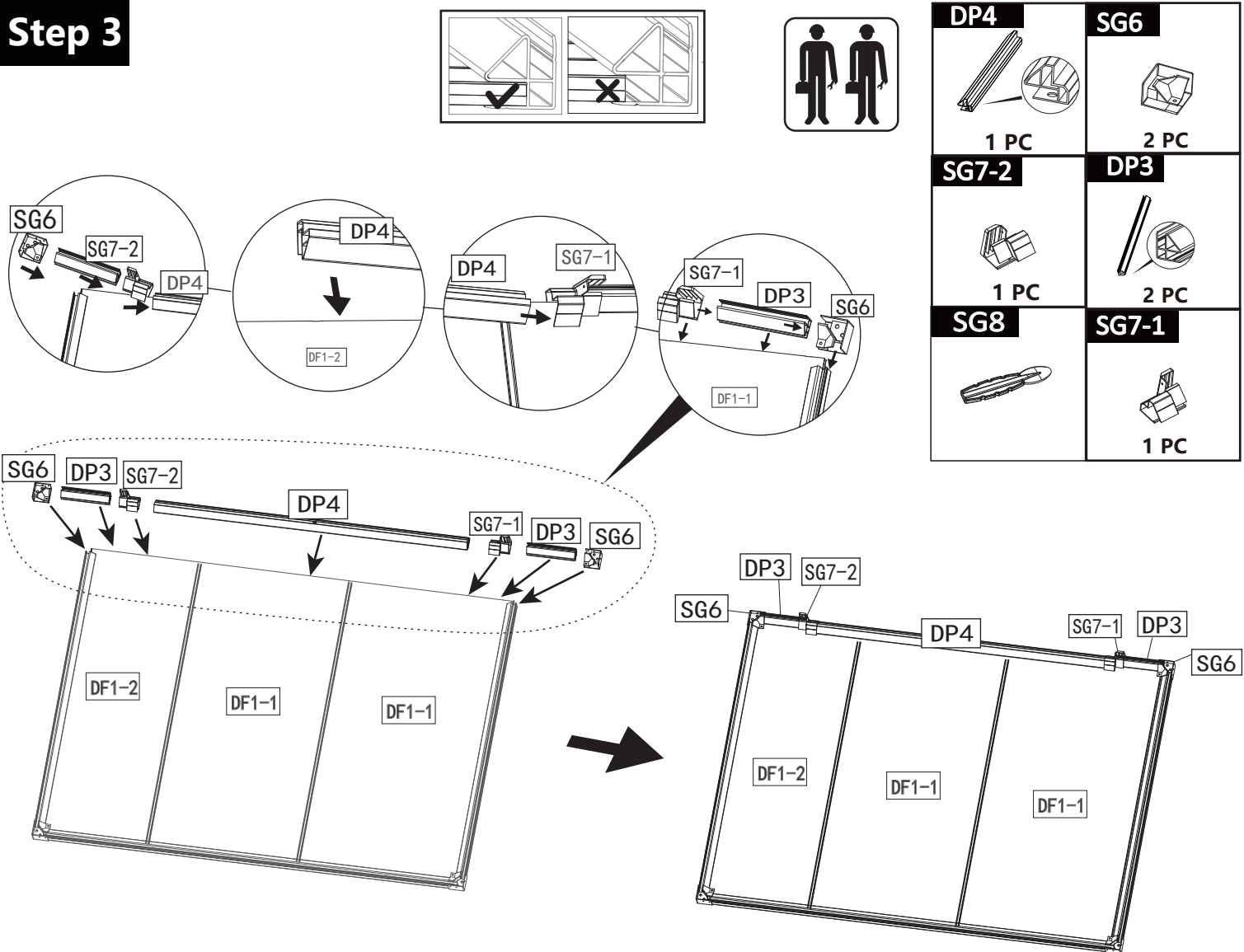
1 PC



Step 2

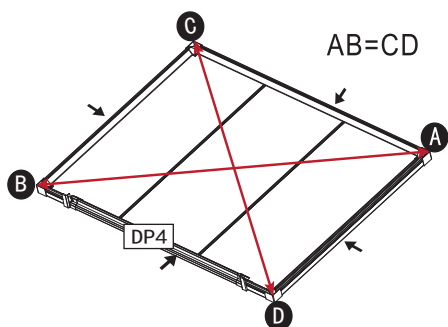


Step 3

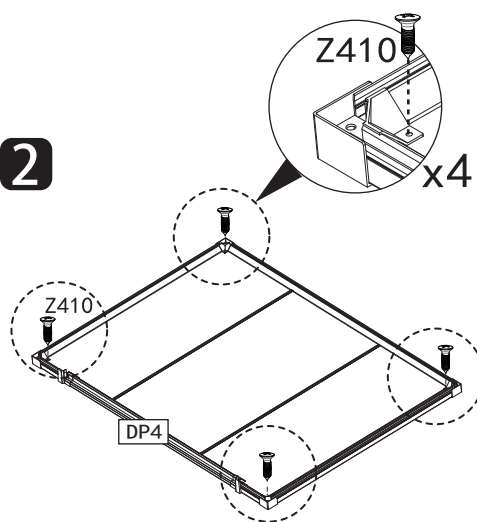


Step 4

1



2



Z410



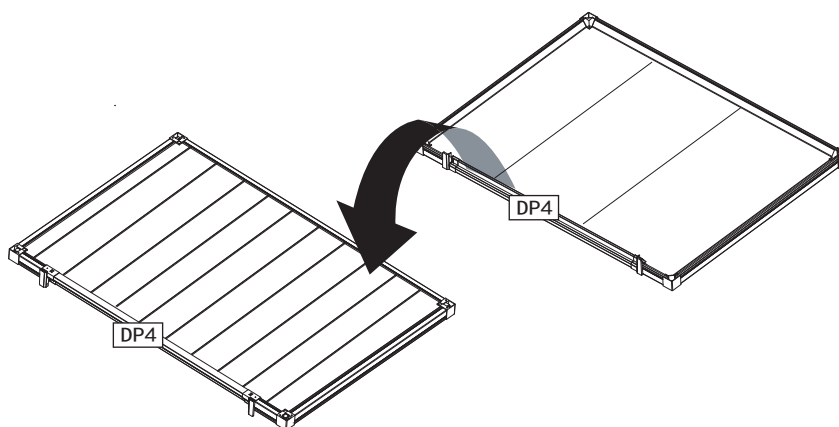
4 PC

Z420

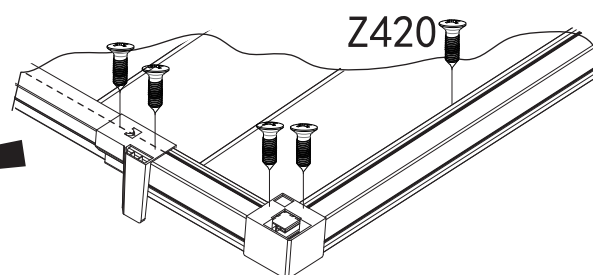
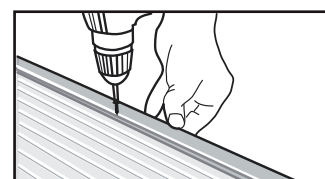
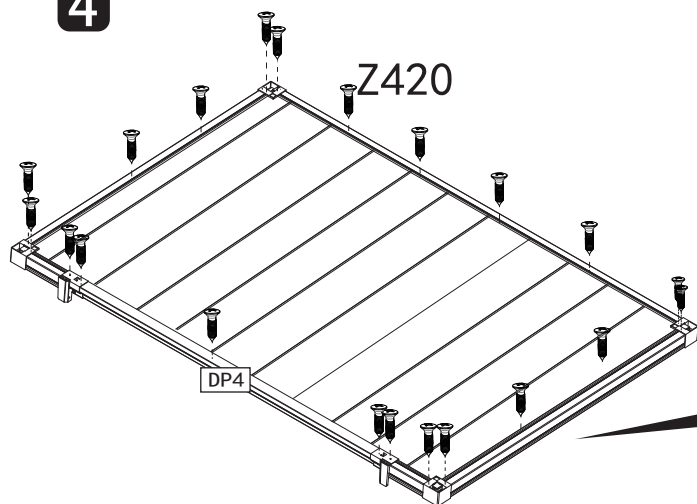


21 PC

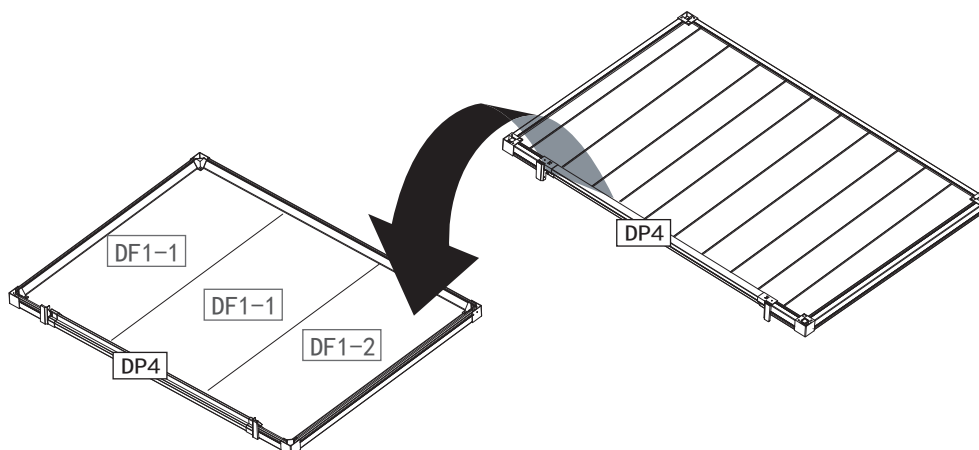
3

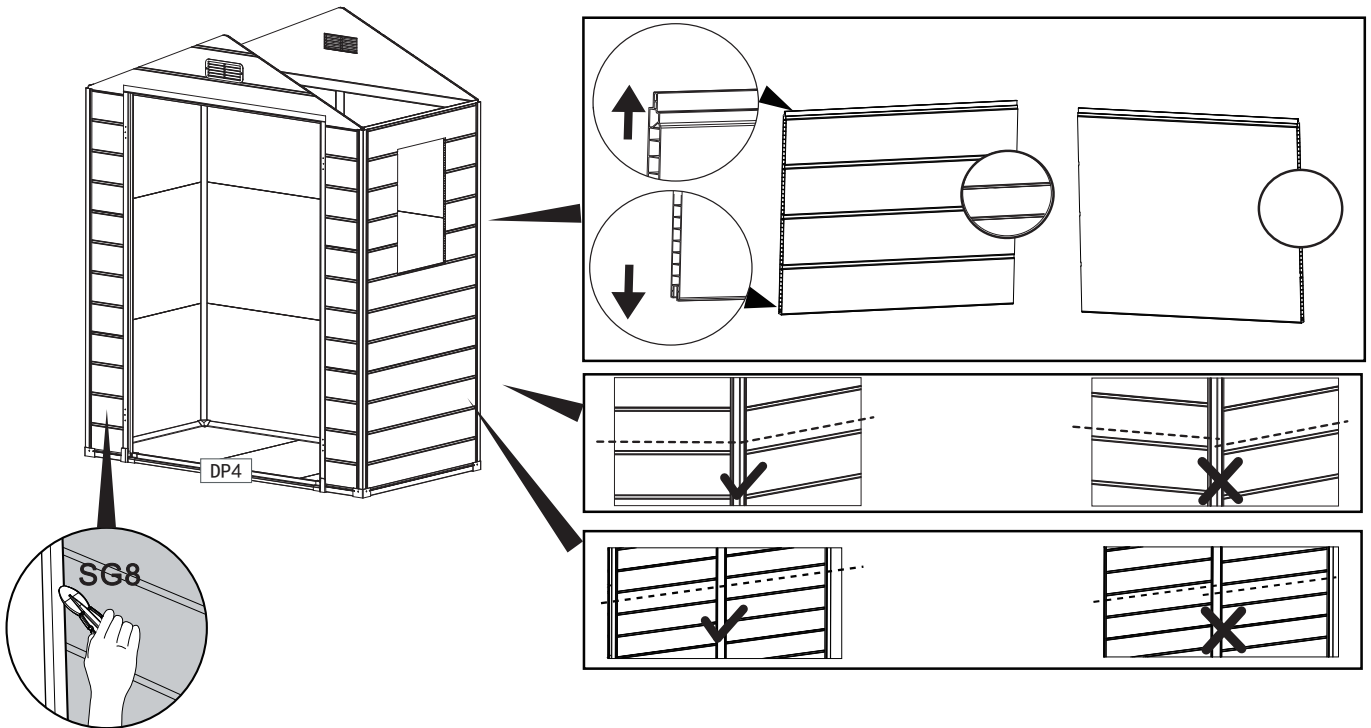


4

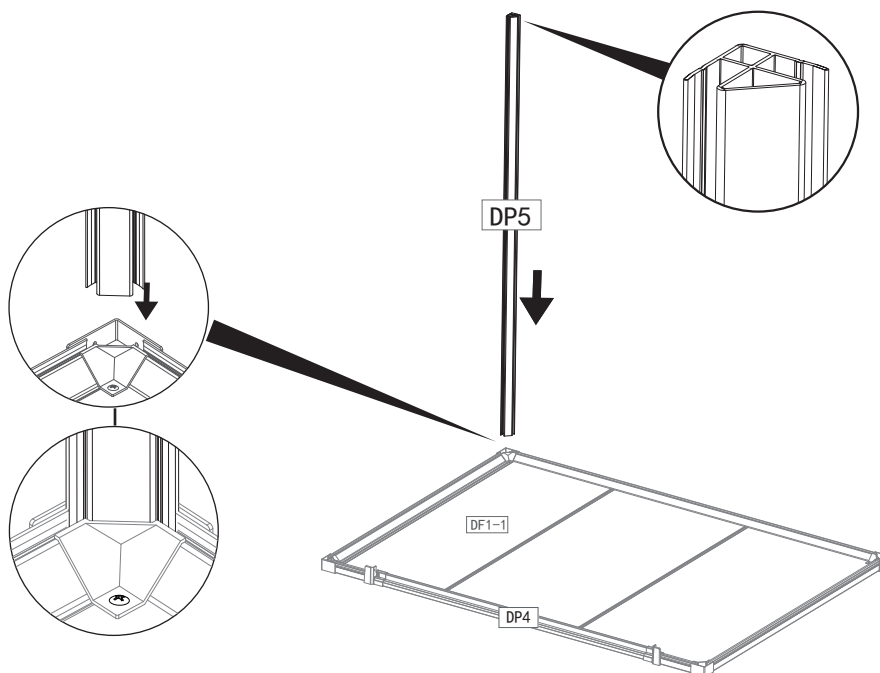
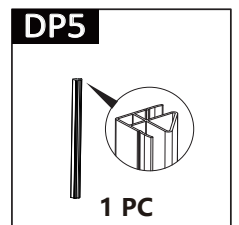


5



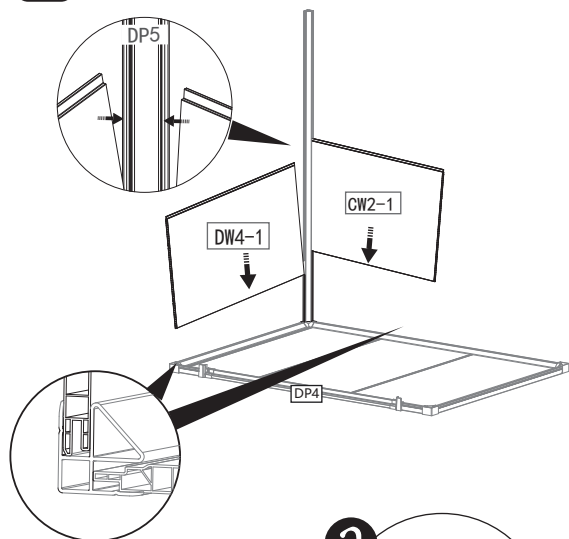


Step 5

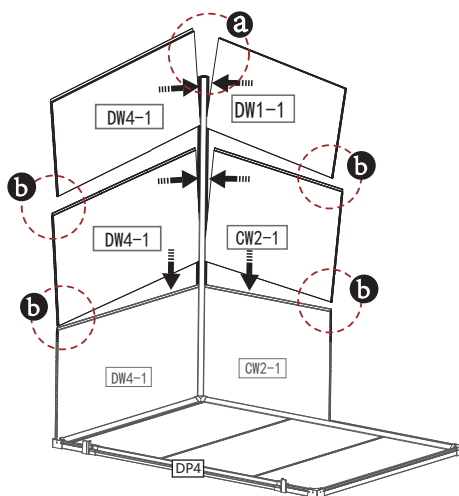


Step 6

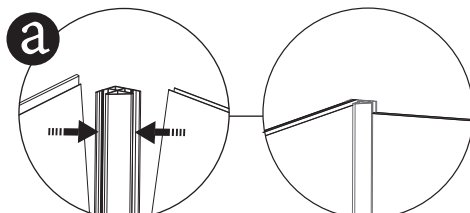
1



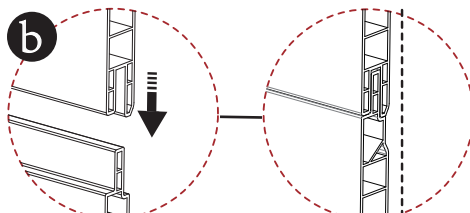
2



a



b

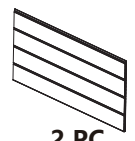


DW4-1



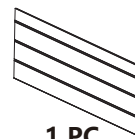
3 PC

CW2-1



2 PC

DW1-1

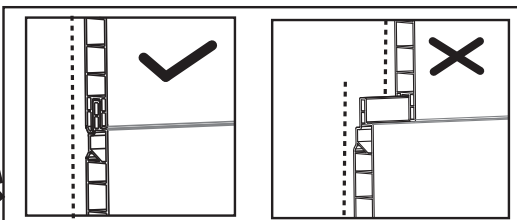
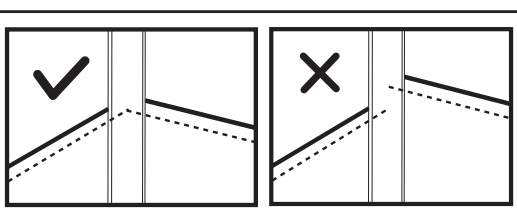
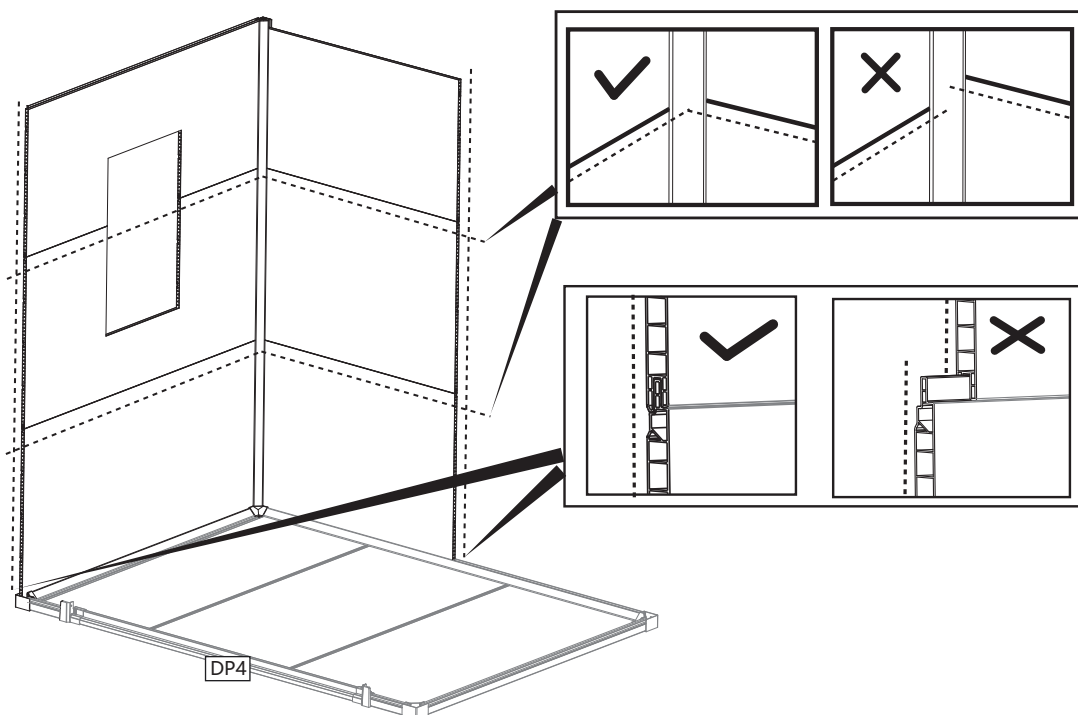


1 PC

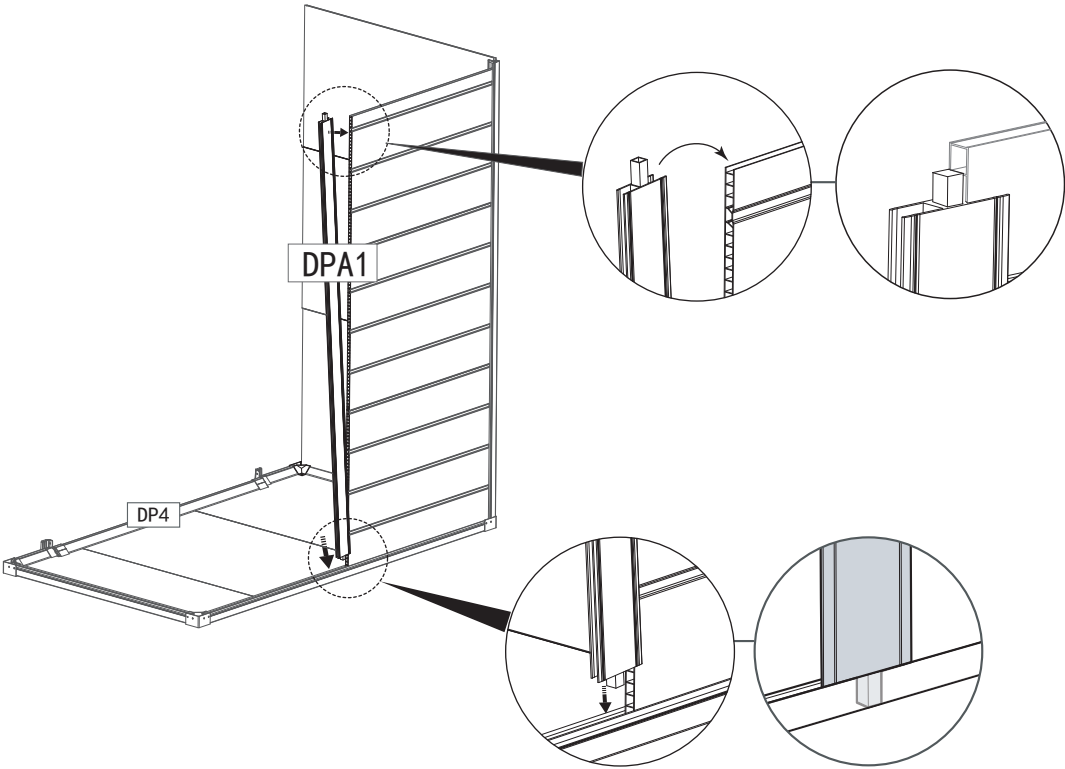
SG8



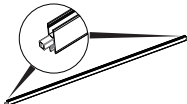
Step 7



Step 8



DPA1

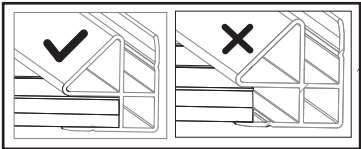
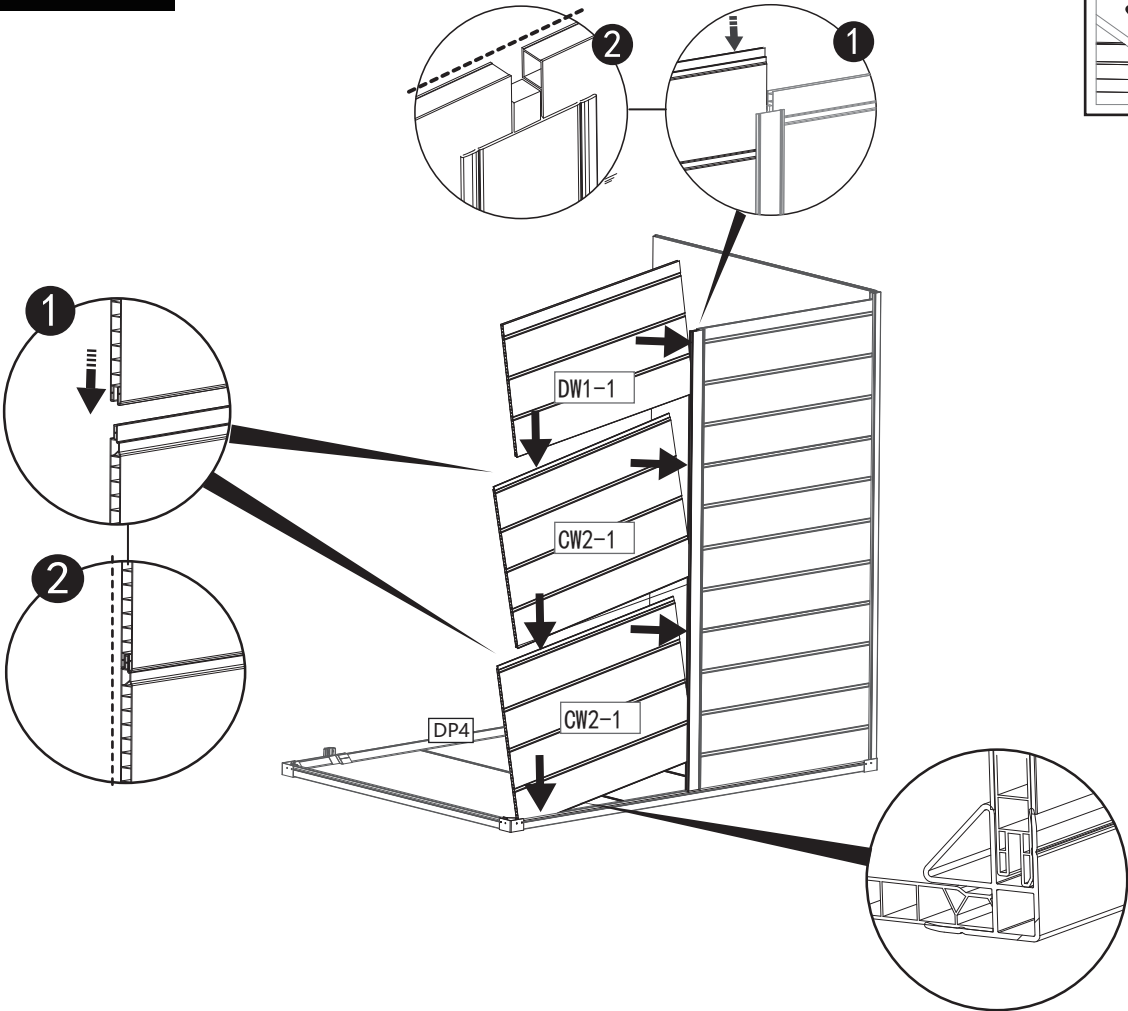


1 PC

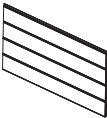
SG8



Step 9

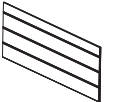


CW2-1



2 PC

DW1-1

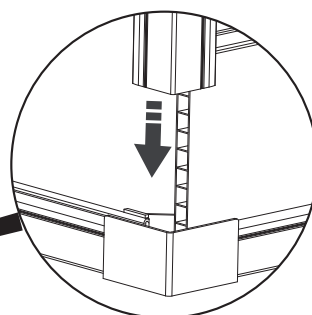
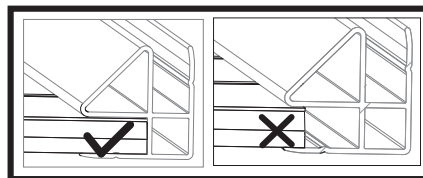
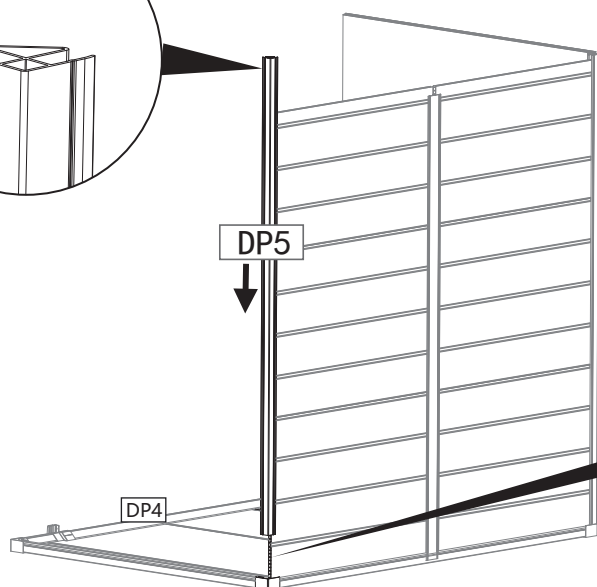
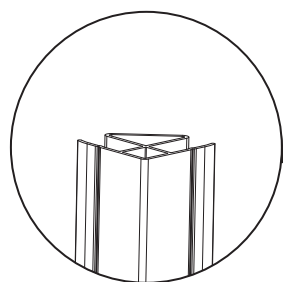


1 PC

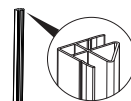
SG8



Step 10

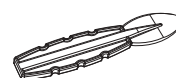


DP5

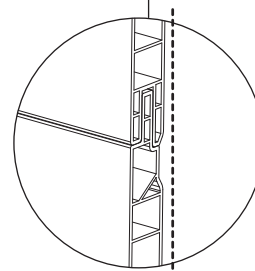
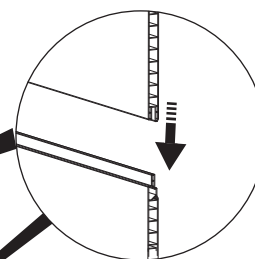
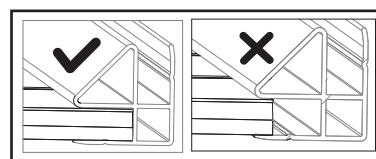
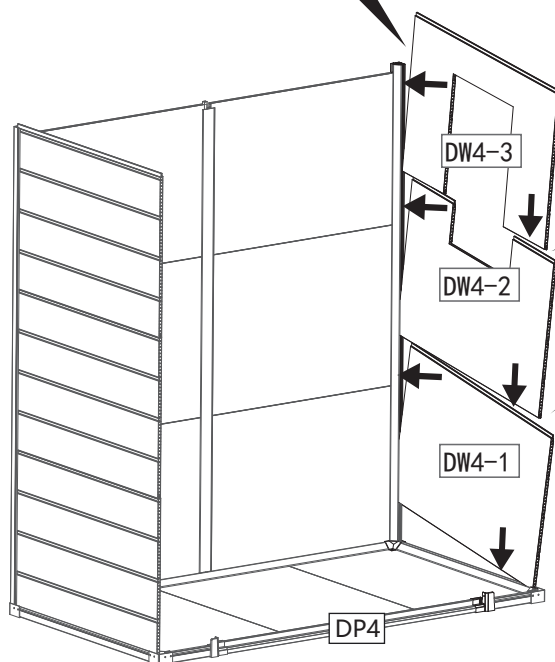
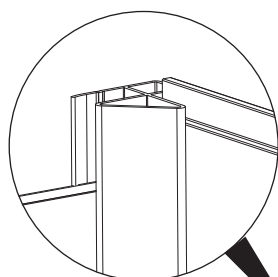


1 PC

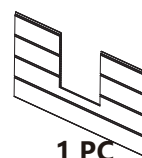
SG8



Step 11

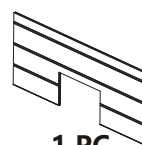


DW4-2



1 PC

DW4-3



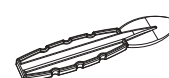
1 PC

DW4-1

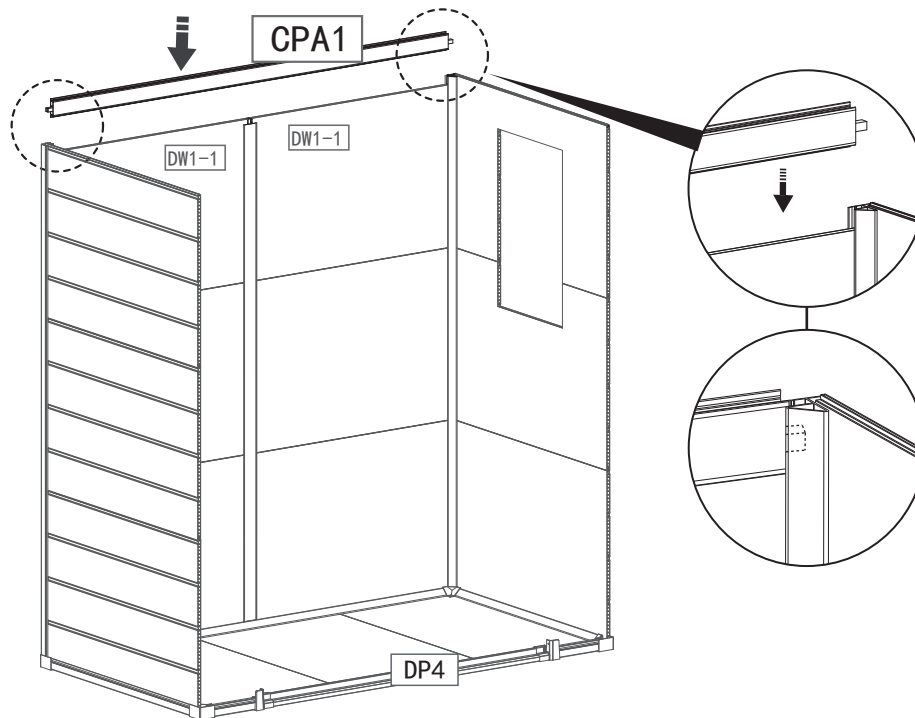
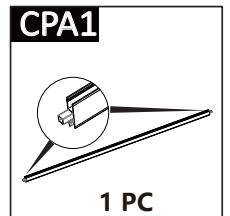


1 PC

SG8

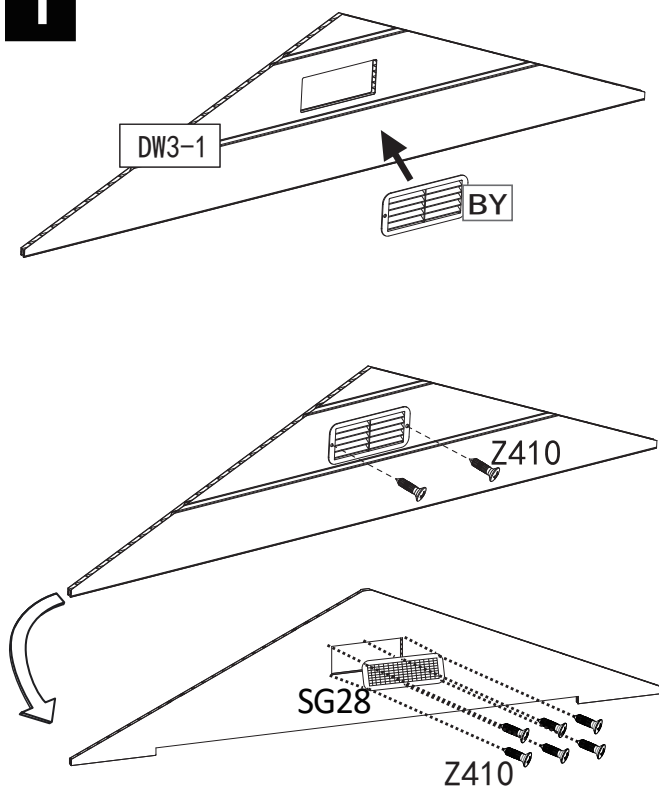


Step 12

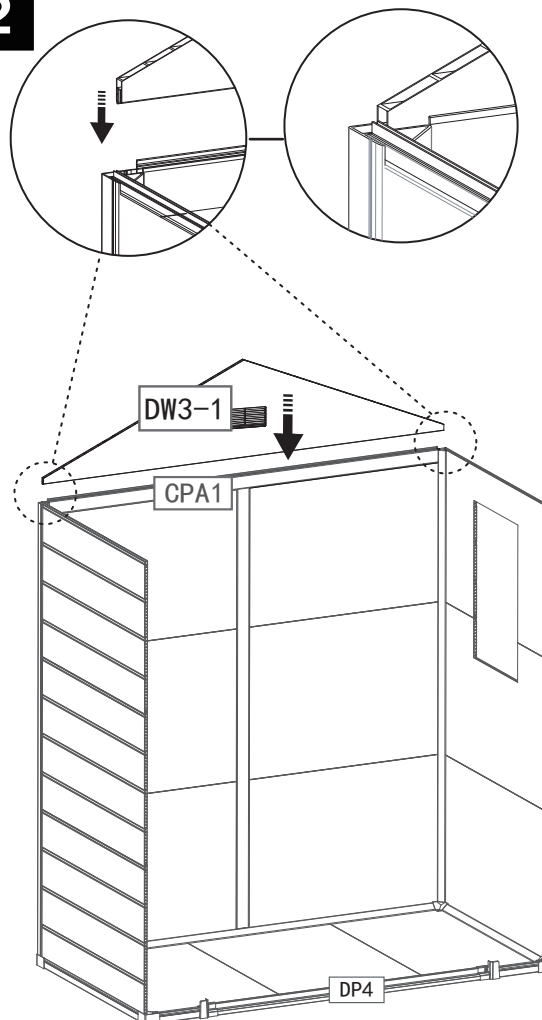


Step 13

1



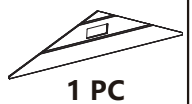
2



BY



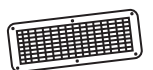
DW3-1



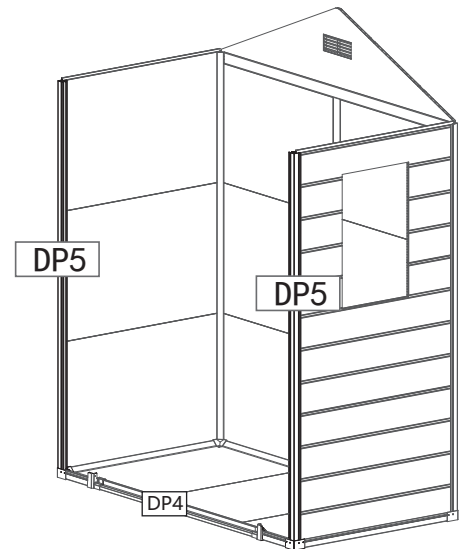
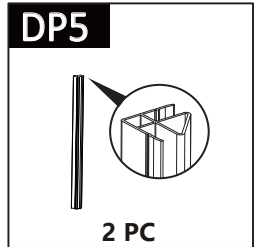
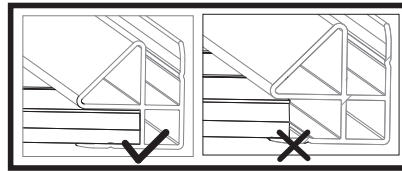
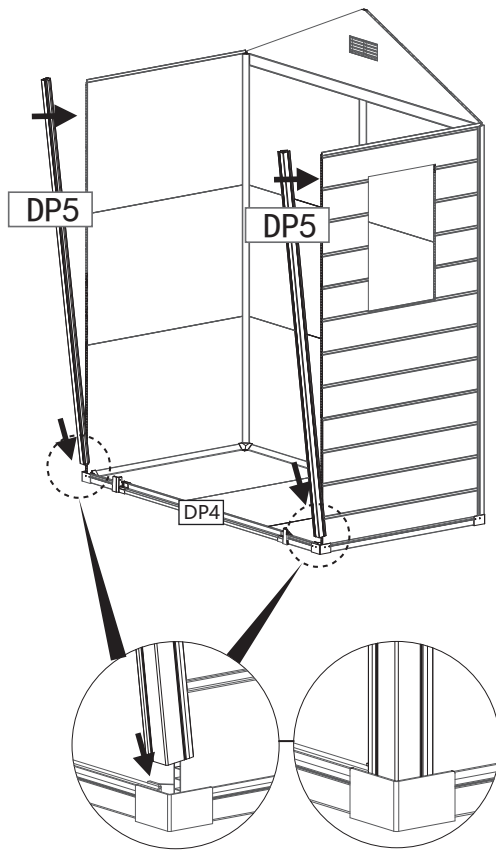
Z410



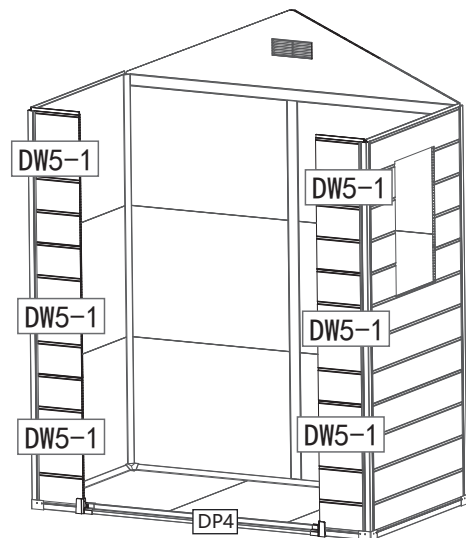
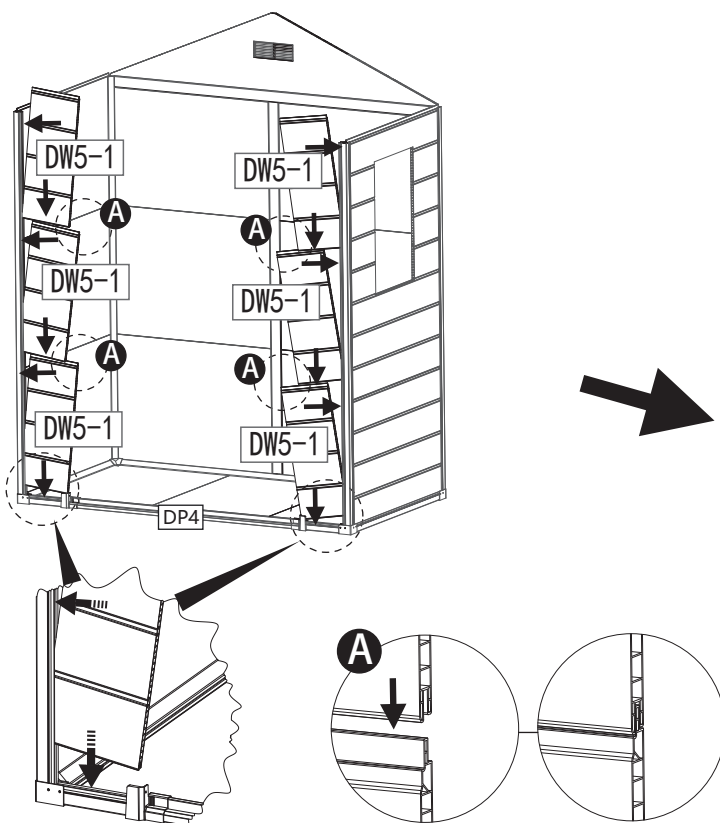
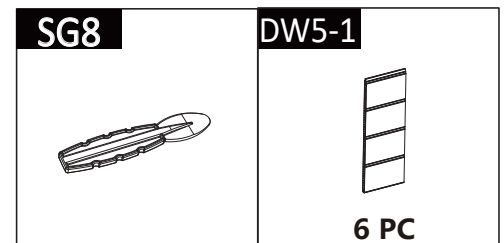
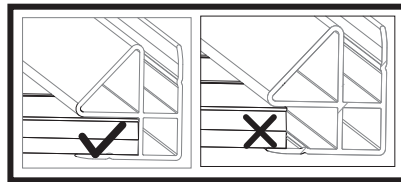
SG28



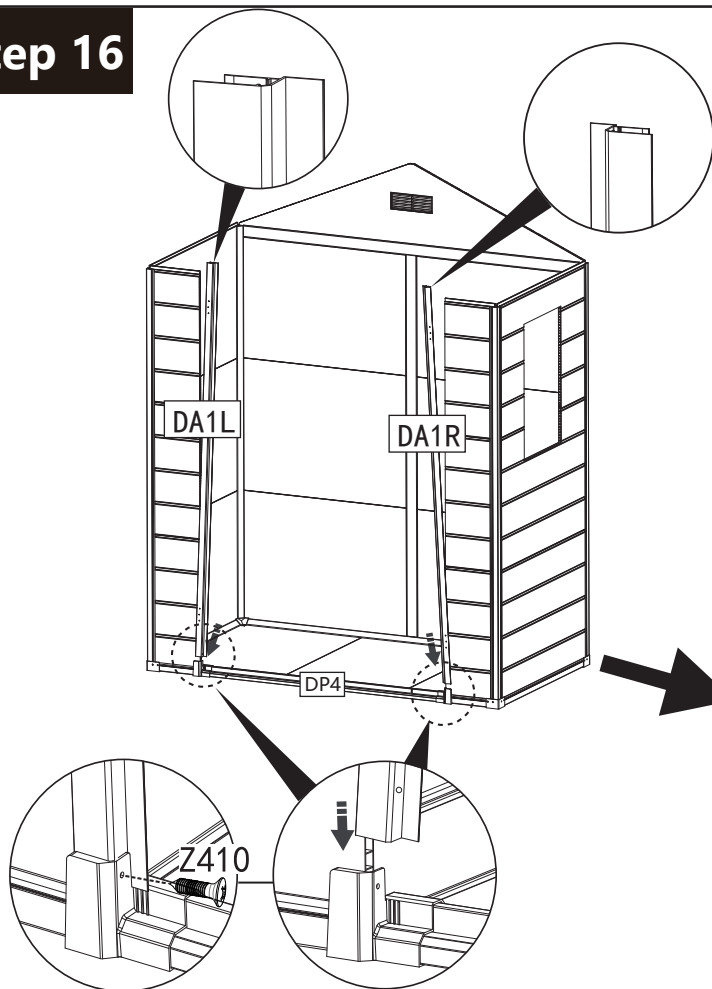
Step 14



Step 15



Step 16



DA1L



1 PC

DA1R



1 PC

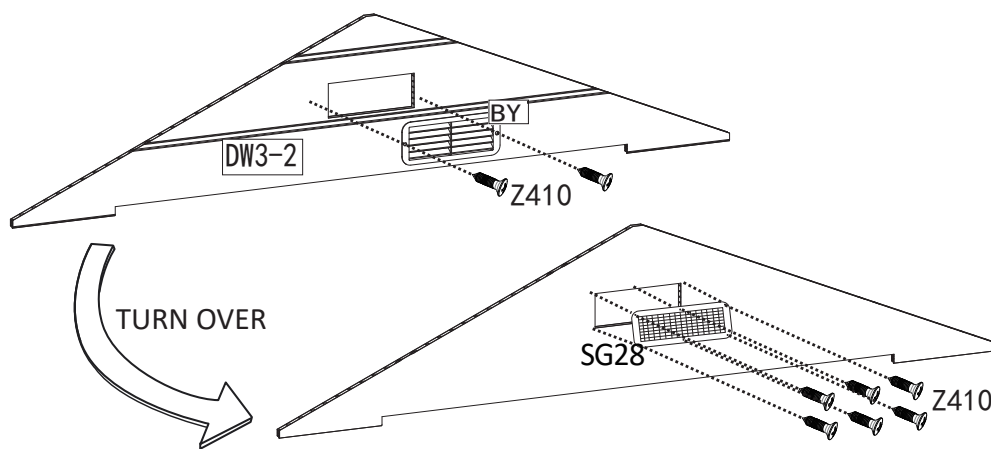
Z410



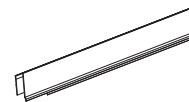
2 PC

Step 17

1



DA3



1 PC

DW3-2



1 PC

BY



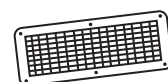
1 PC

Z410



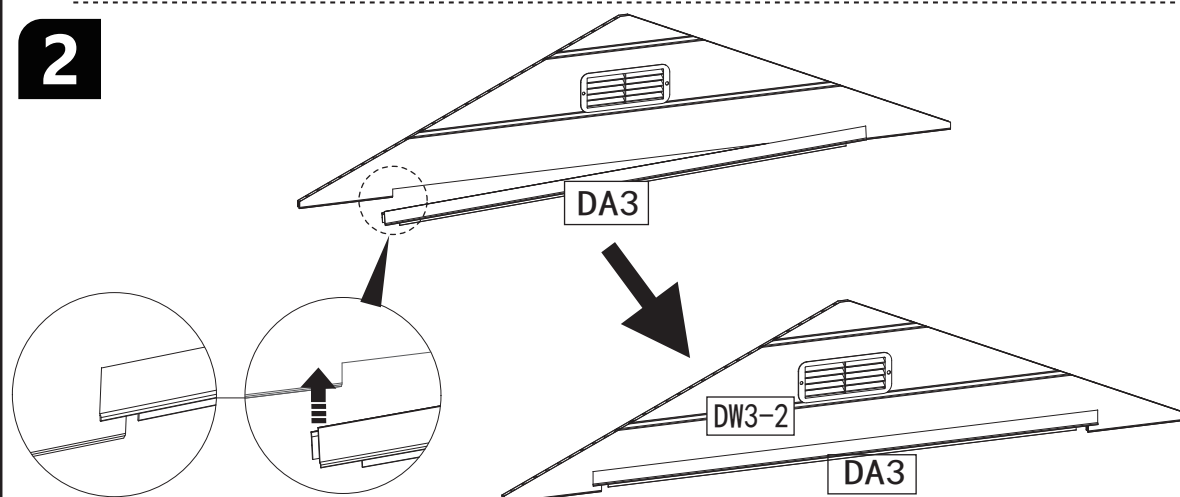
8 PC

SG28

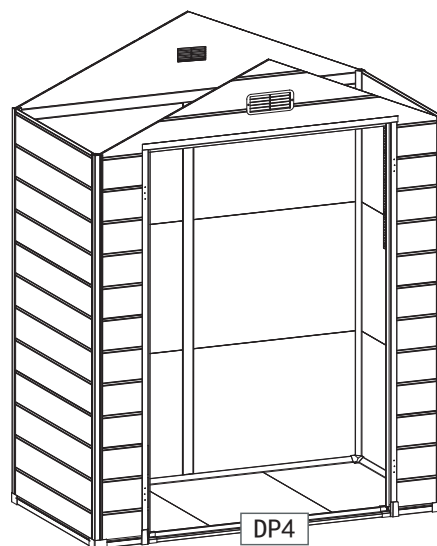
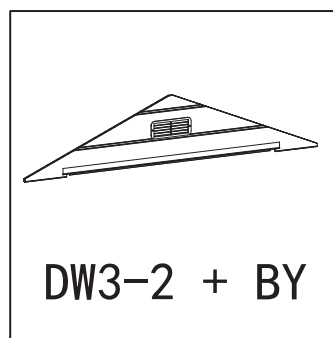
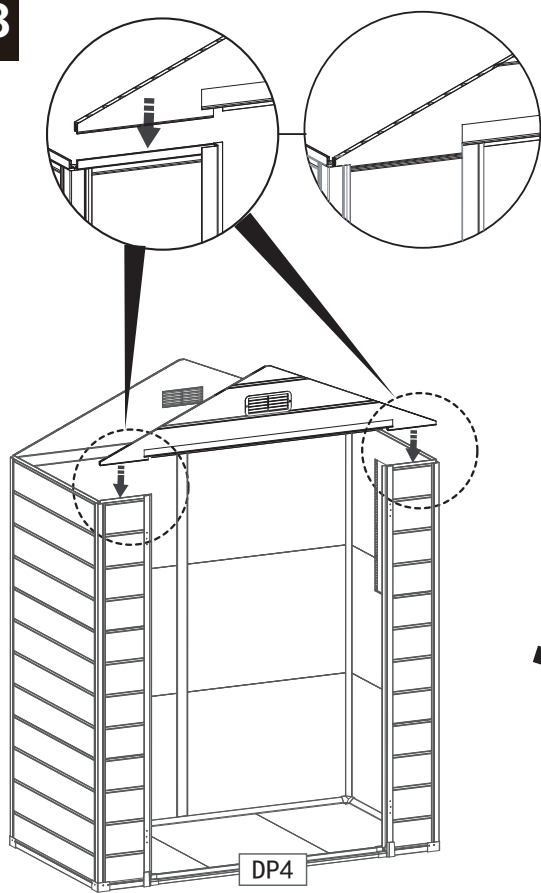


1 PC

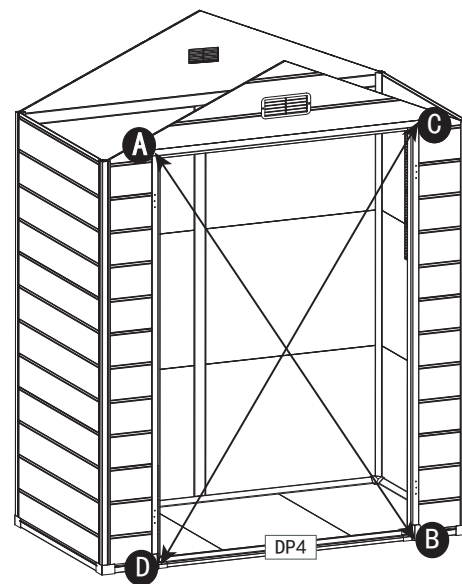
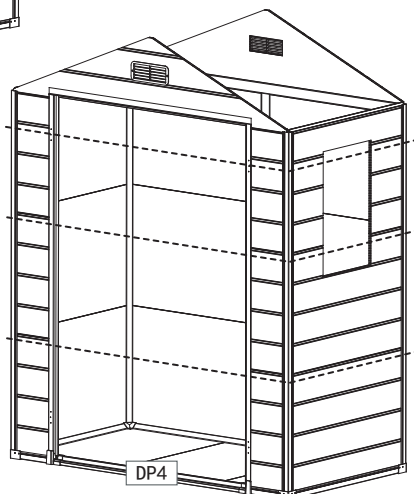
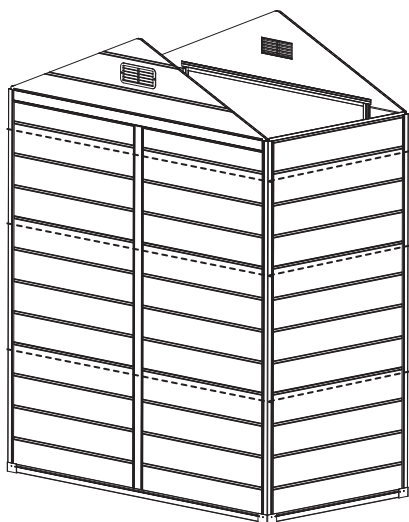
2



Step 18

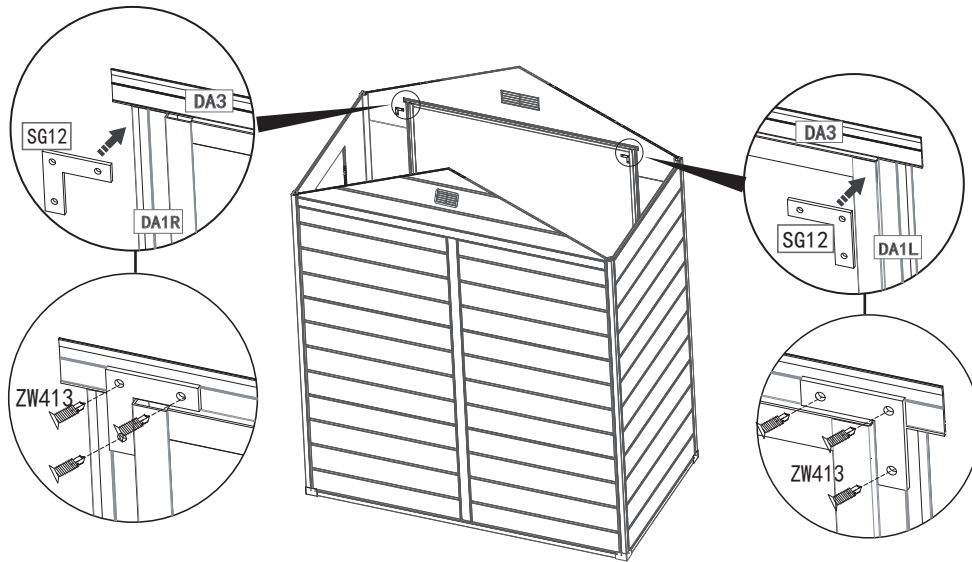


Step 19

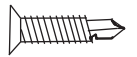


$$AB=CD$$

Step 20



ZW413

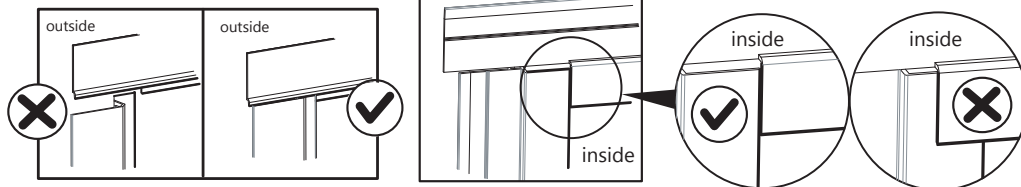


6 PC

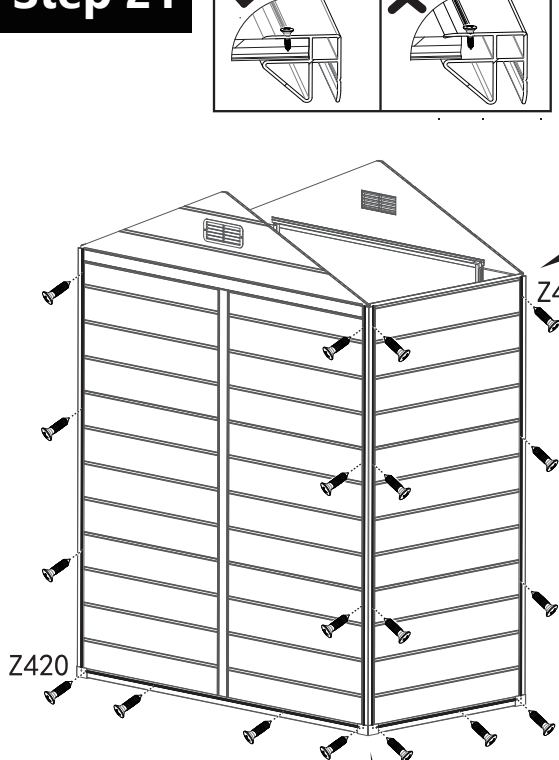
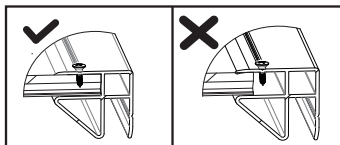
SG12



2 PC



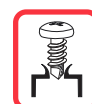
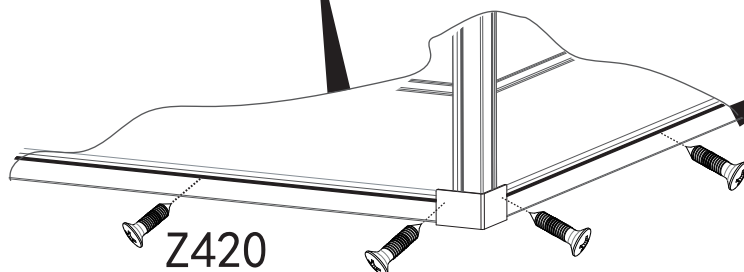
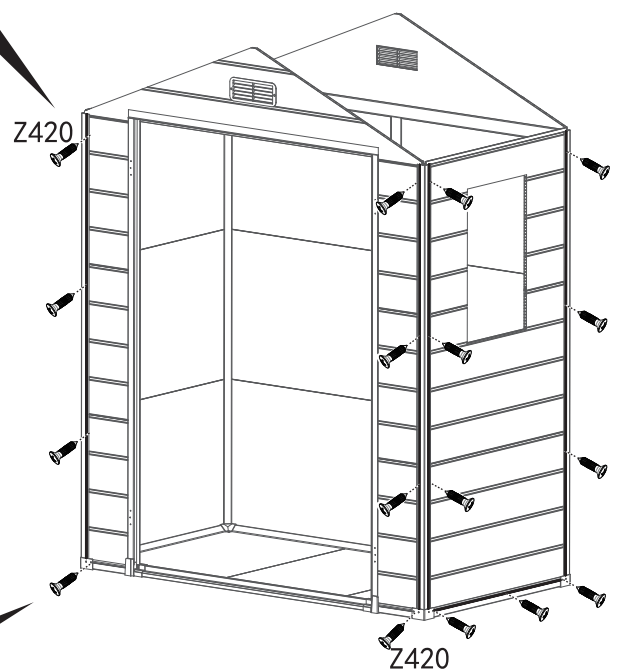
Step 21



Z420

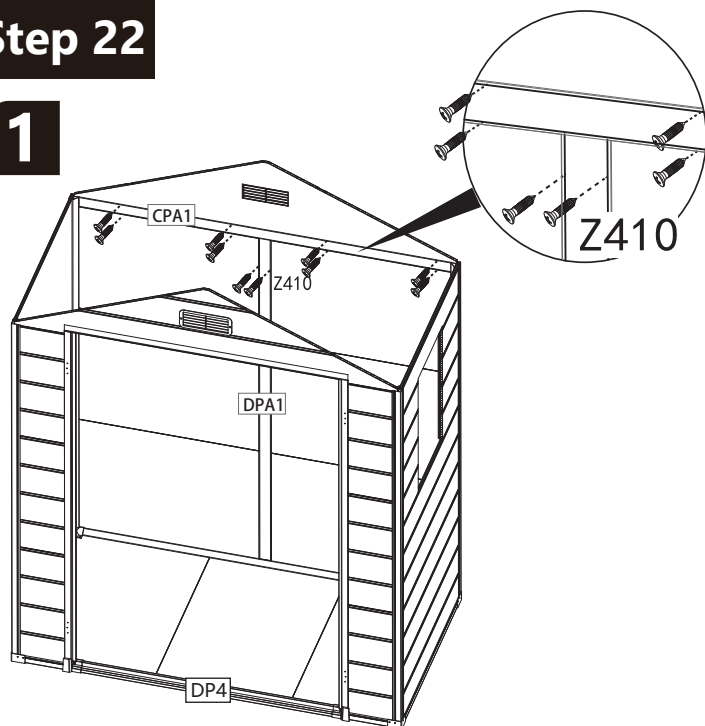


36 PC



Step 22

1



Z410



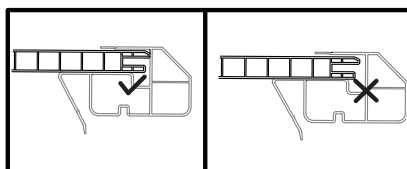
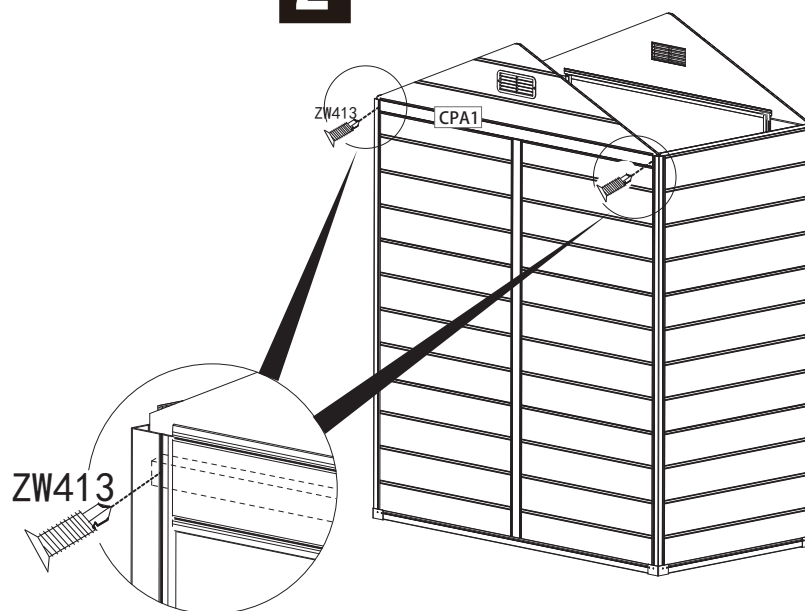
10 PC

ZW413

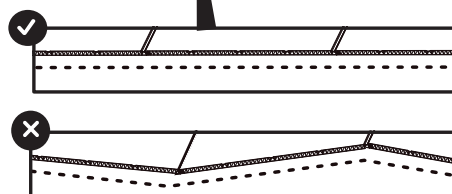
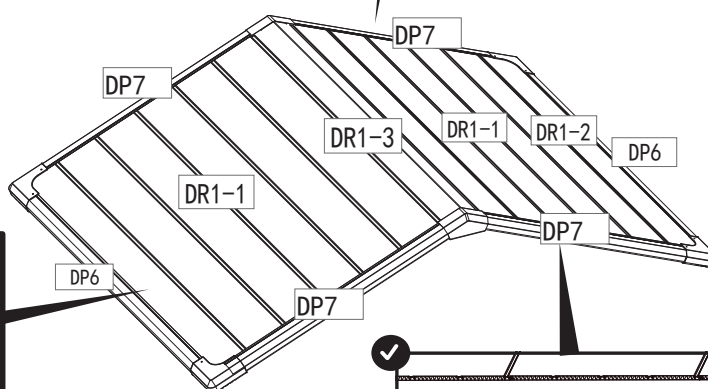
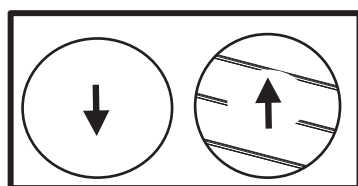


2 PC

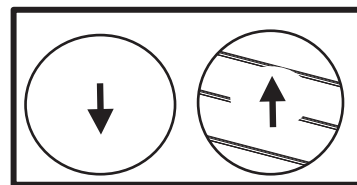
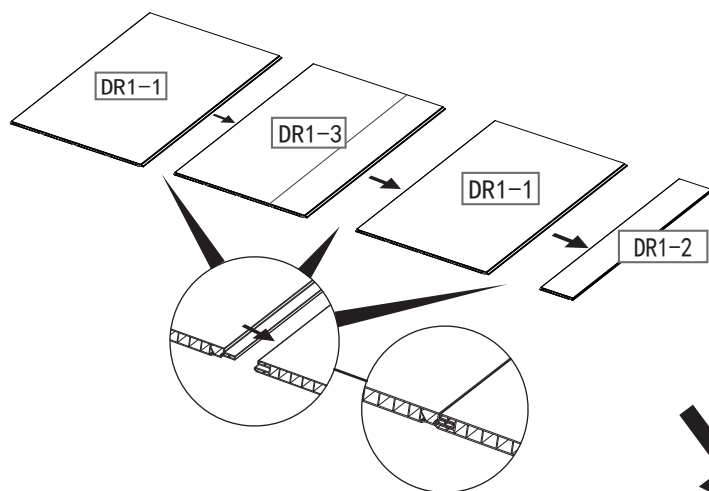
2



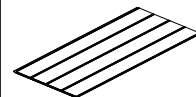
SG8



Step 23

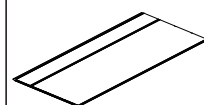


DR1-1



2 PC

DR1-3

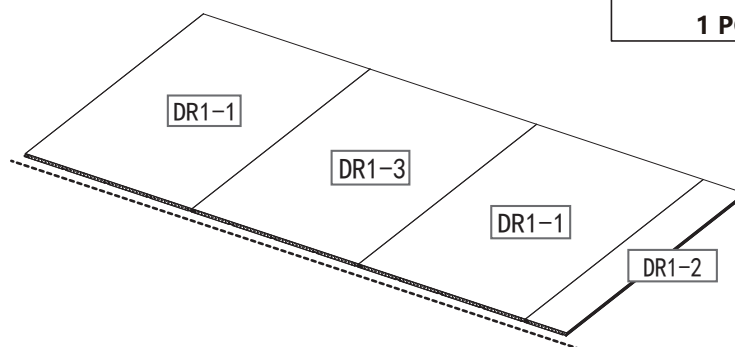
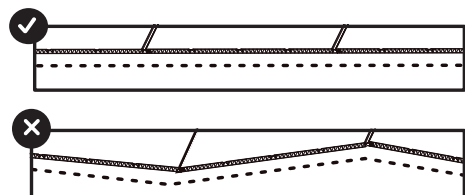


1 PC

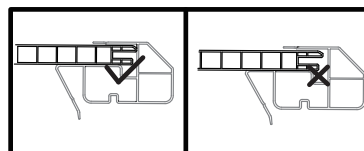
DR1-2



1 PC



Step 24



SG8



DP6



2 PC

DP7

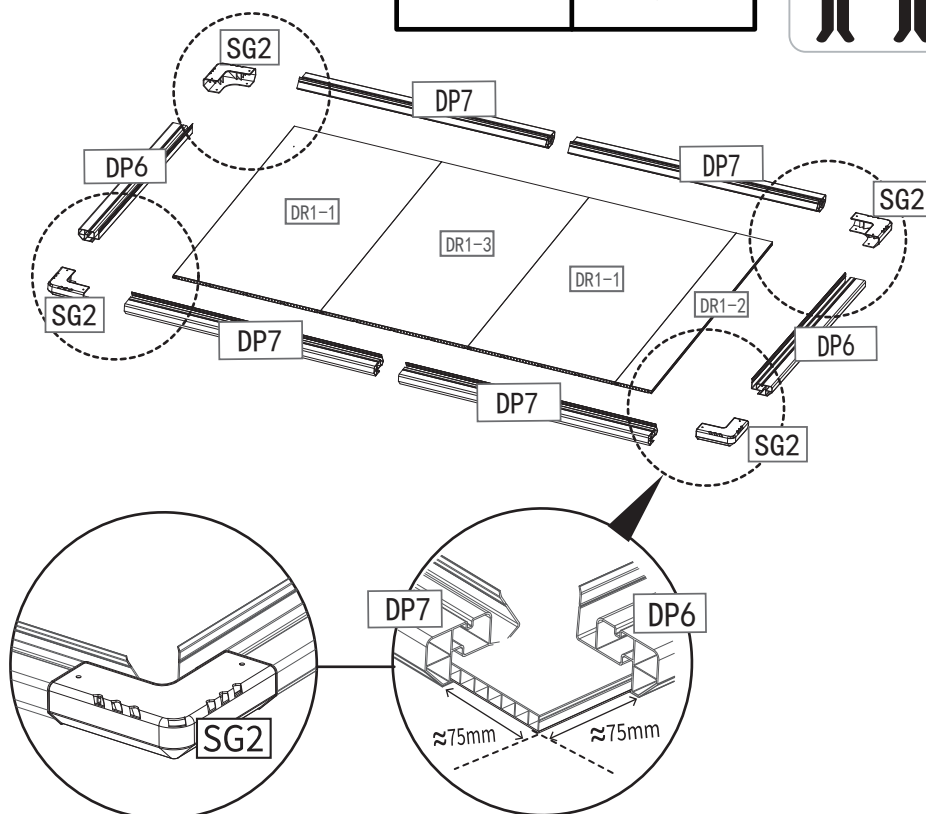


4 PC

SG2



4 PC



Step 25



Z410



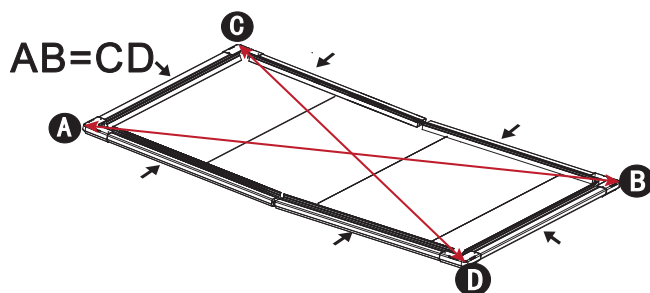
8 PC

Z420

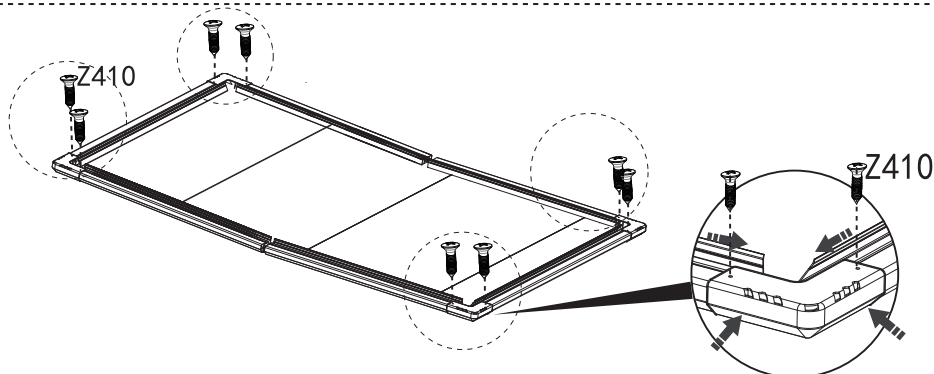


20 PC

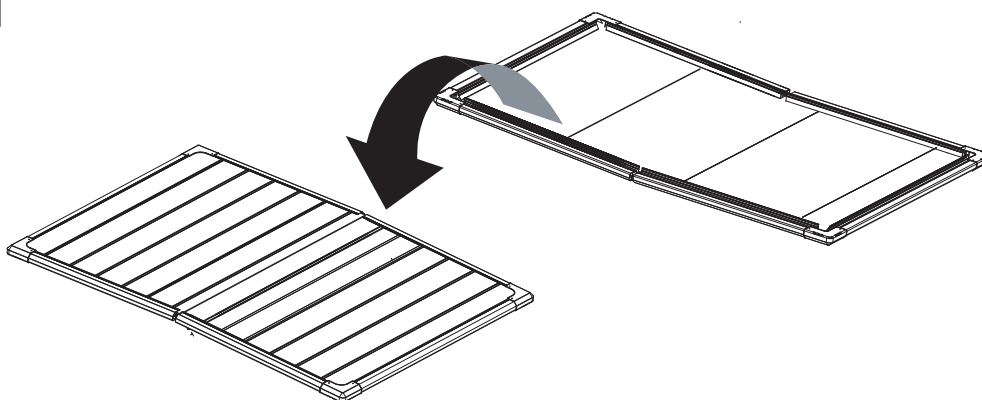
1



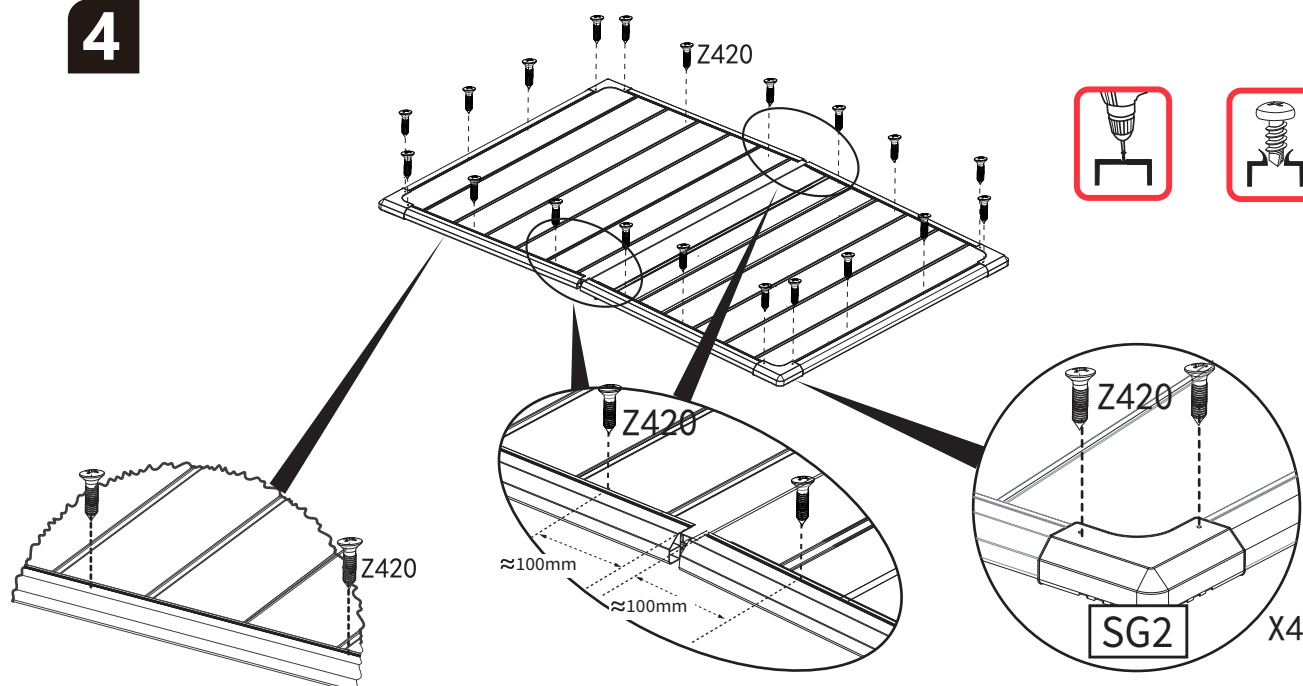
2



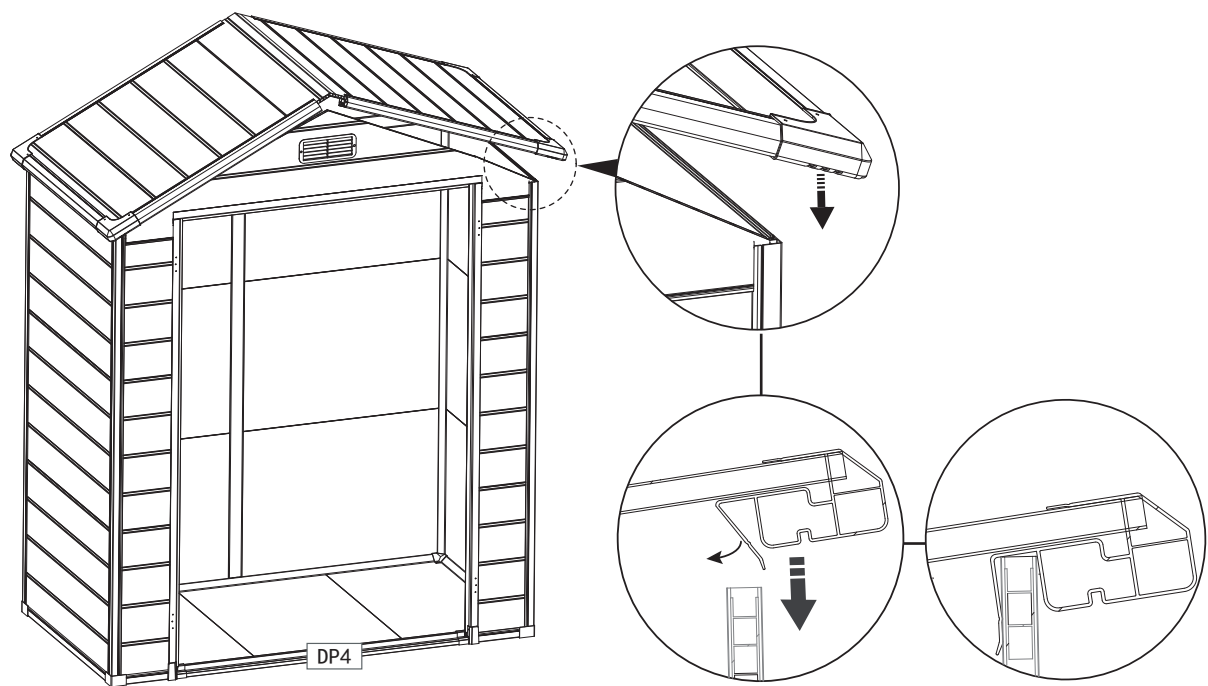
3



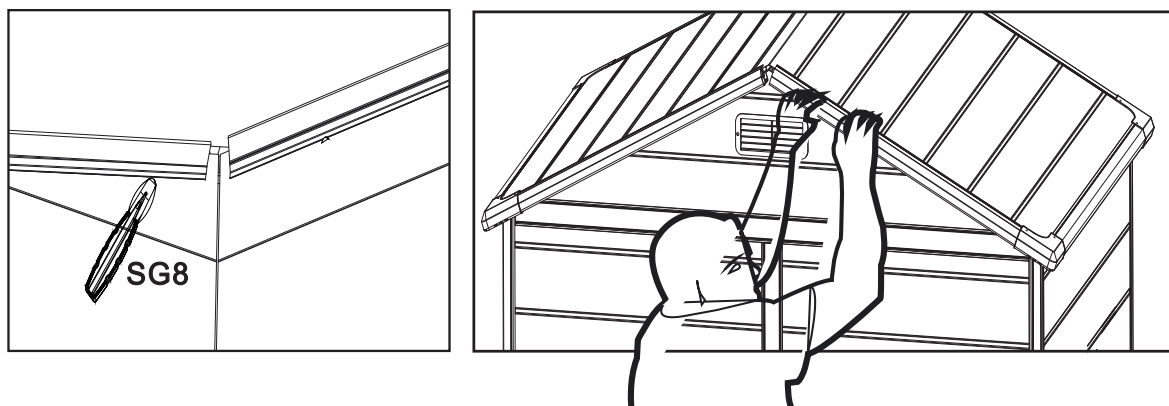
4



Step 26



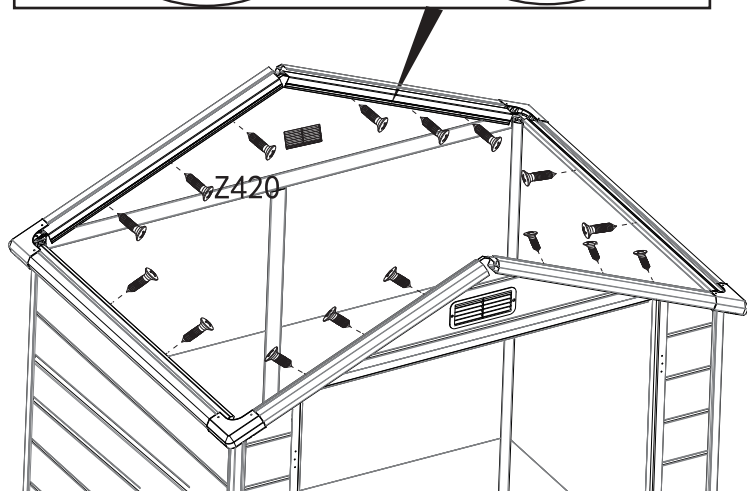
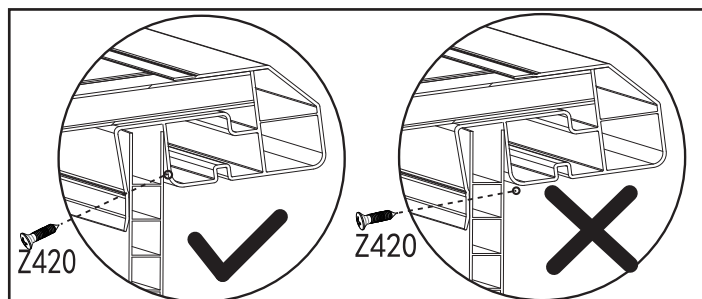
Step 27



SG8



Step 28



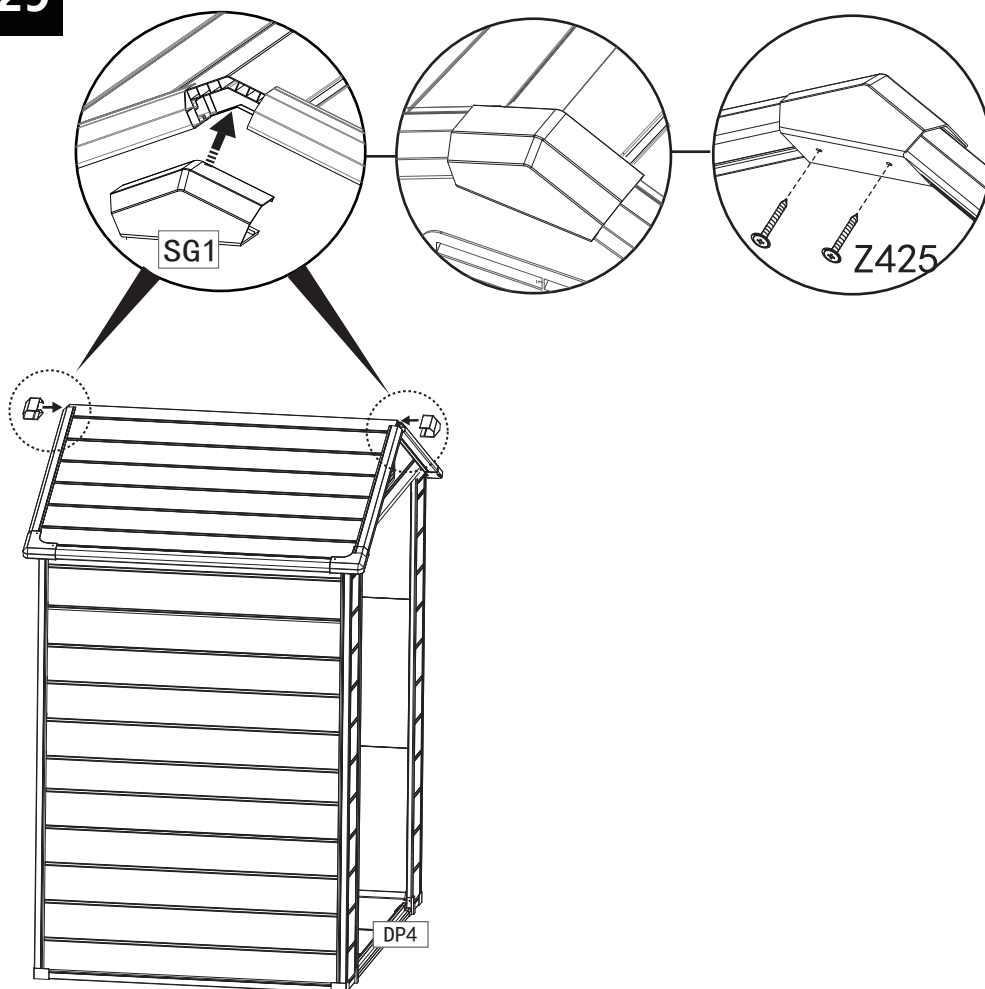
Z420



16 PC



Step 29



Z425



4 PC

SG1

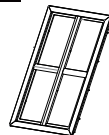


2 PC

Step 30

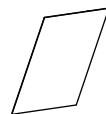


SG10



1 PC

SG101



1 PC

SG102

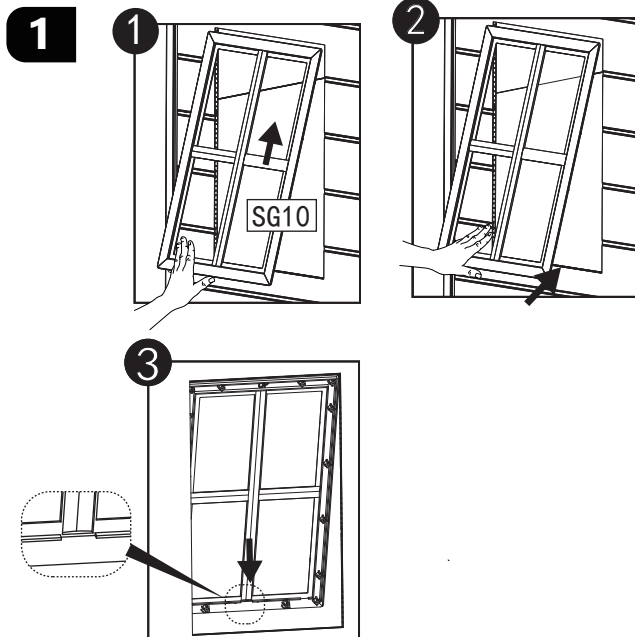
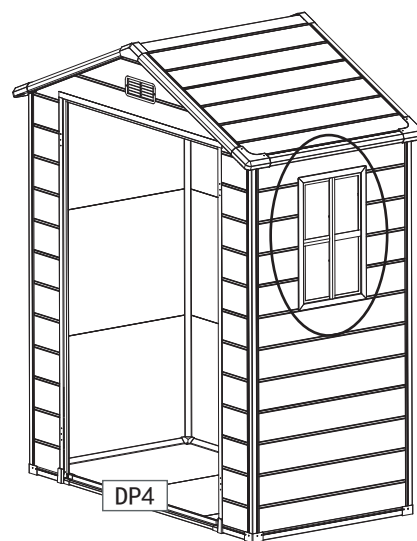


1 PC

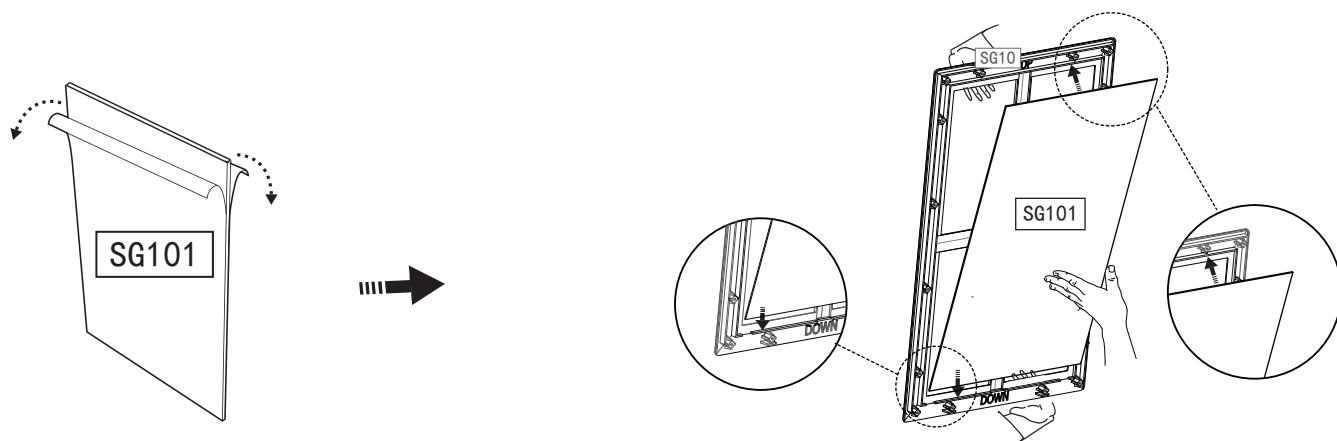
Z410



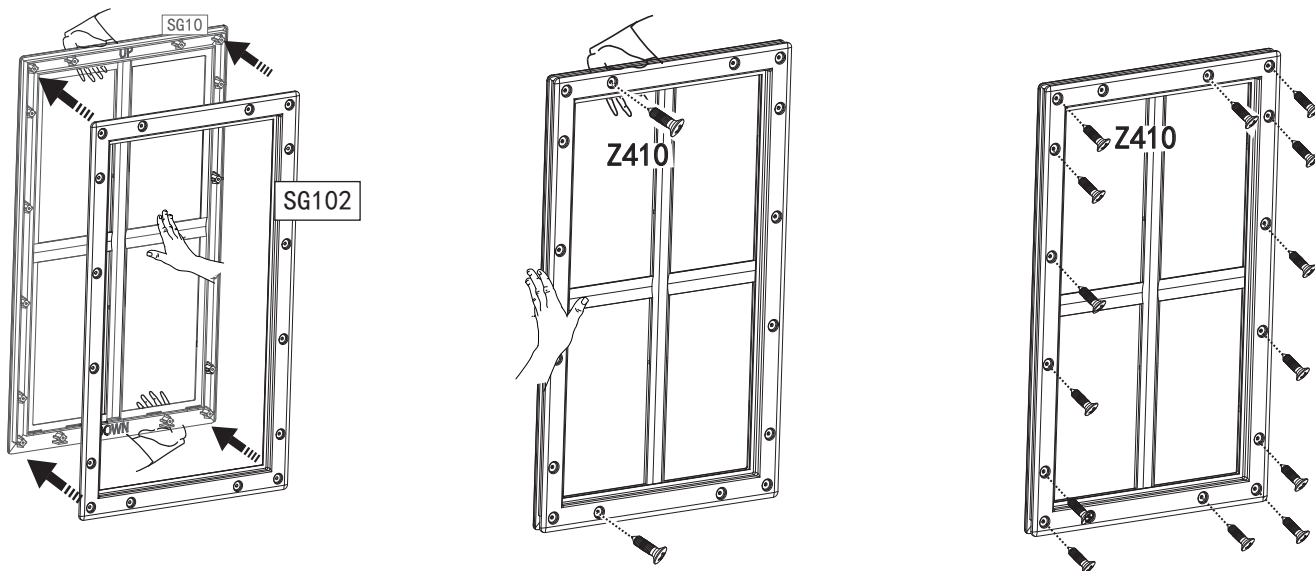
16 PC



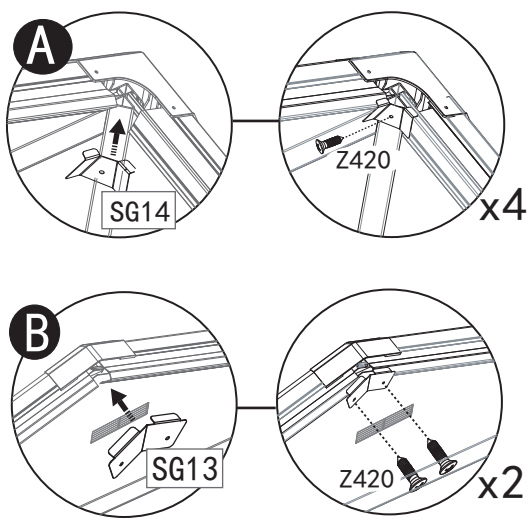
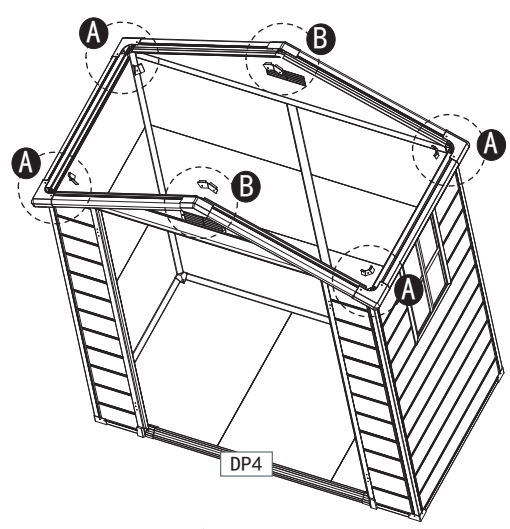
2

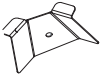
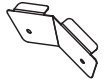


3

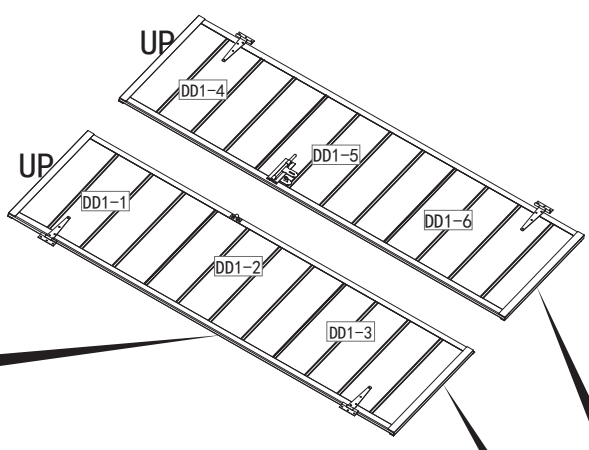
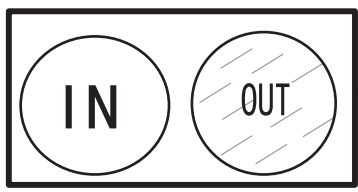
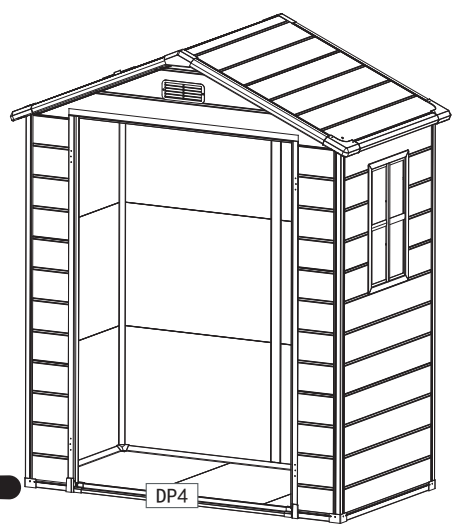


Step 31



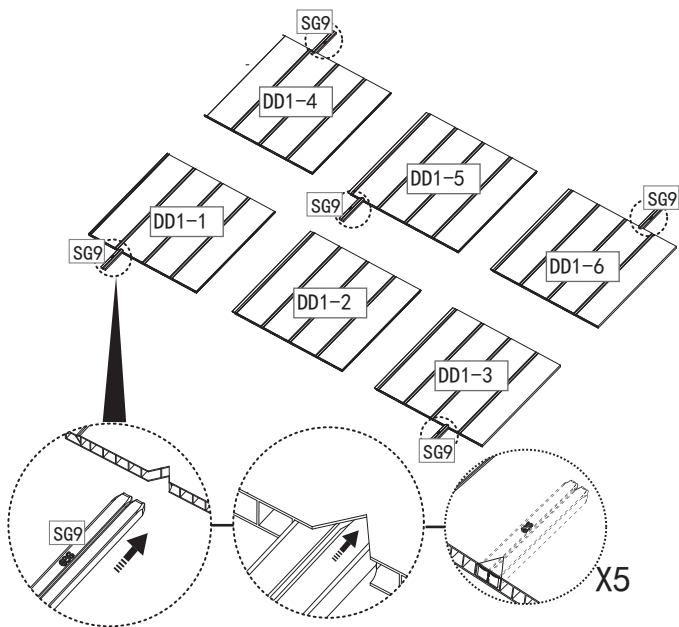
SG14

4 PC
SG13

2 PC
Z420

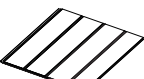
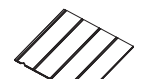
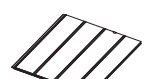
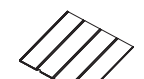
8 PC



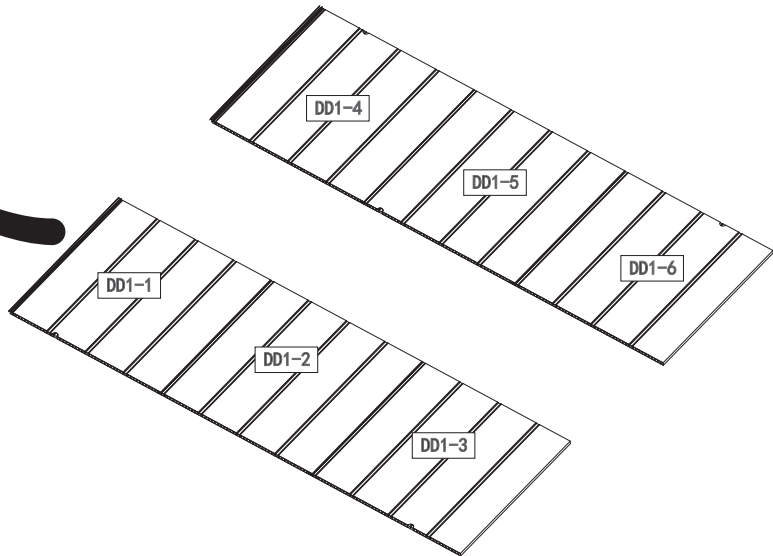
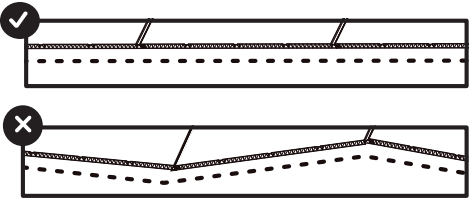
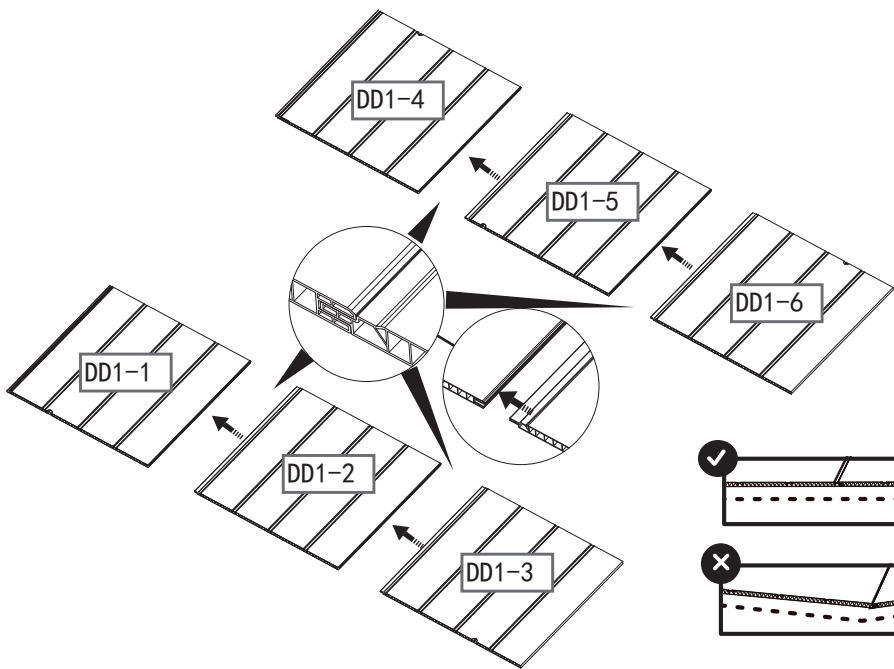
Step 32

1

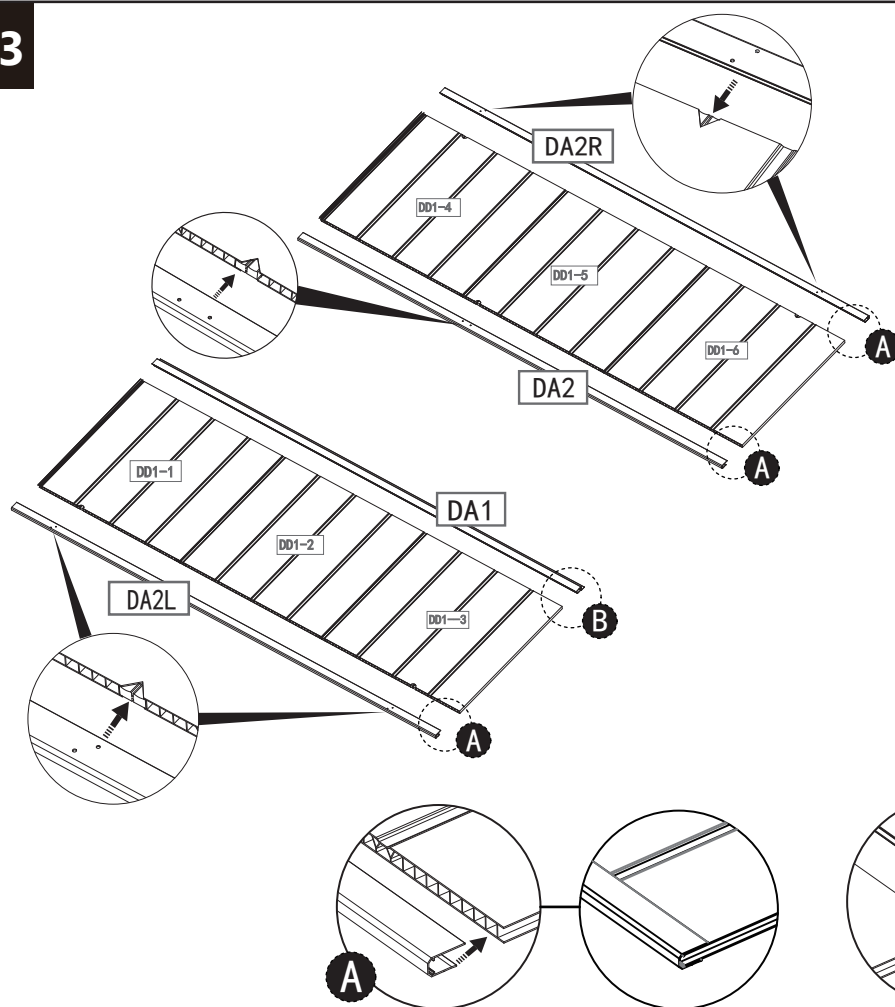


DD1-3  1 PC	DD1-2  1 PC
DD1-5  1 PC	DD1-6  1 PC
	DD1-1  1 PC
	DD1-4  1 PC
	SG9  5 PC

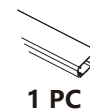
2



Step 33

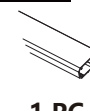


DA2R



1 PC

DA2



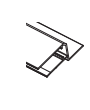
1 PC

DA2L

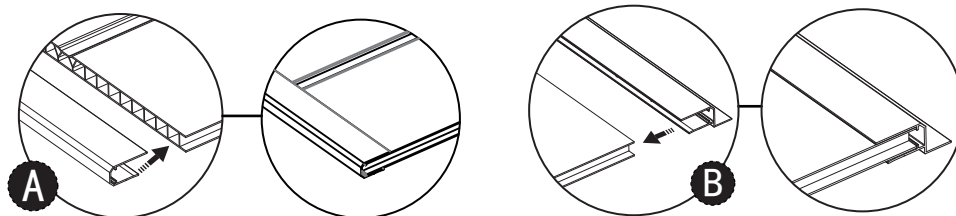


1 PC

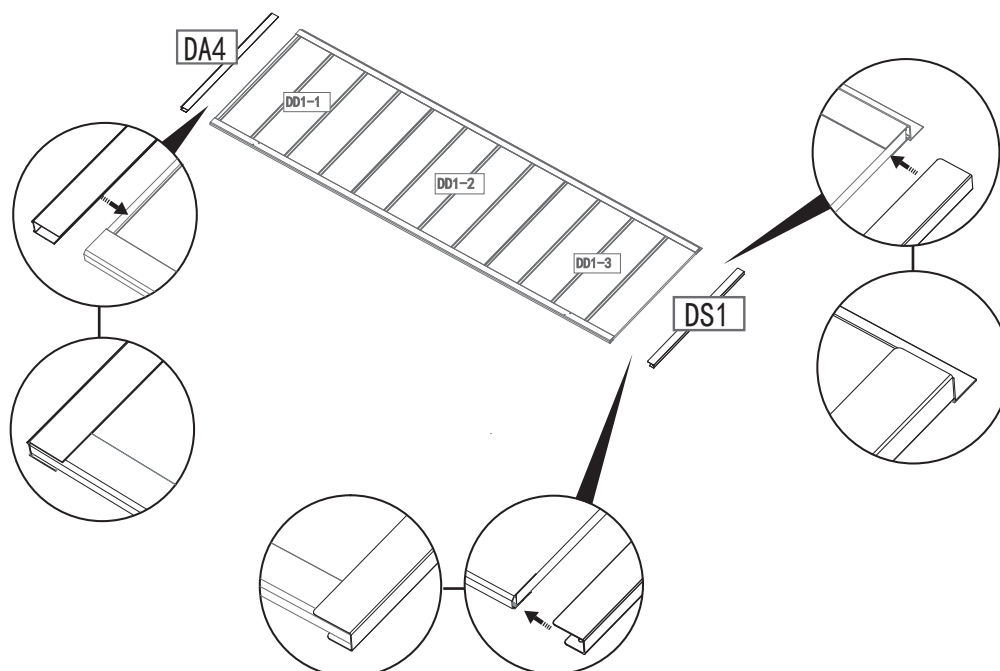
DA1



1 PC



Step 34

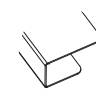


DA4



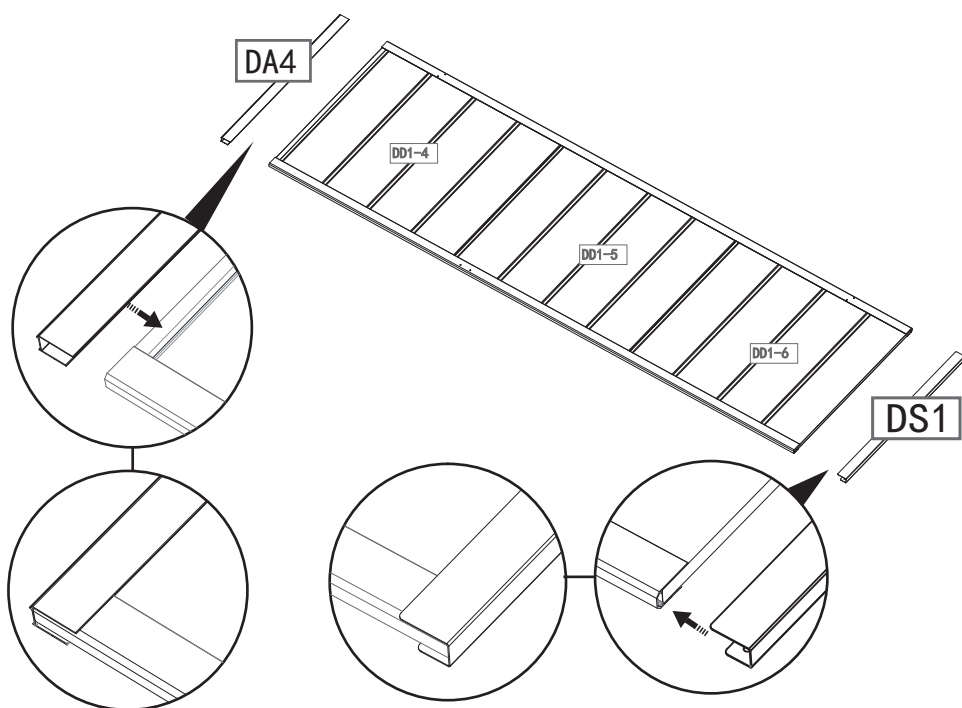
1 PC

DS1

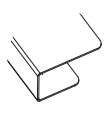


1 PC

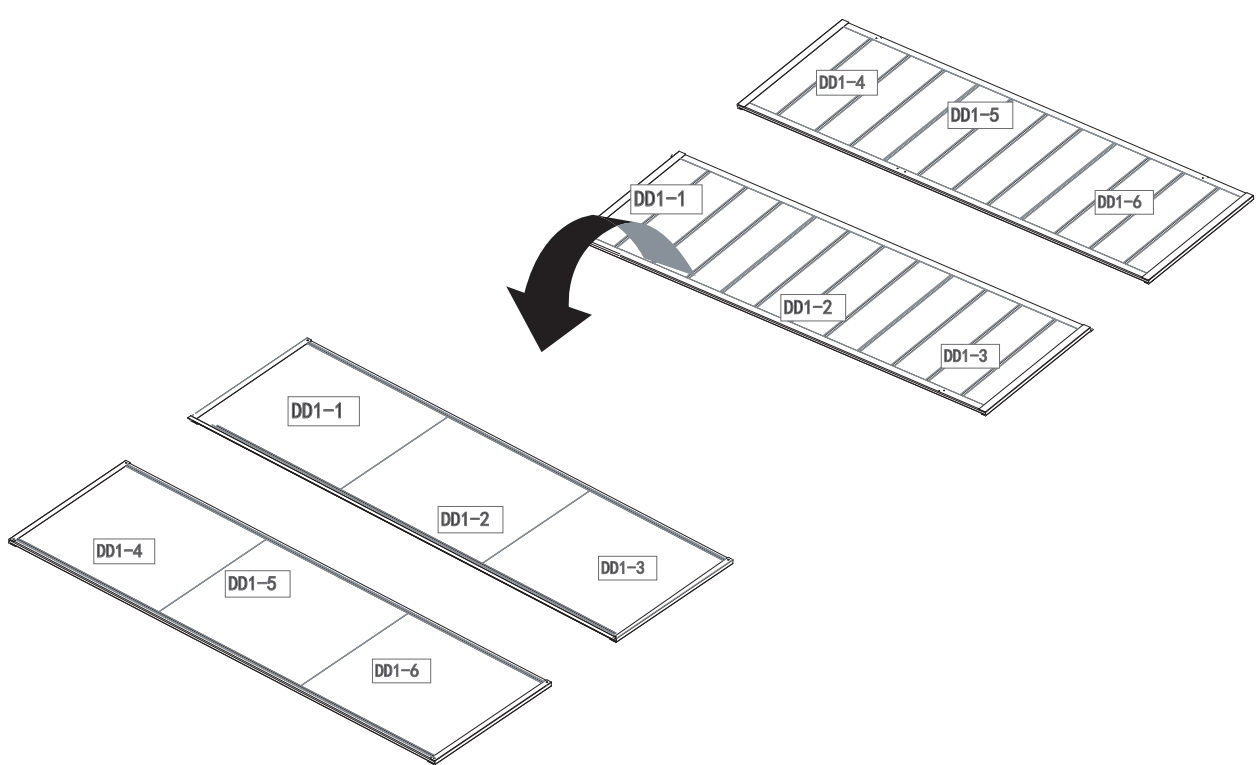
Step 35



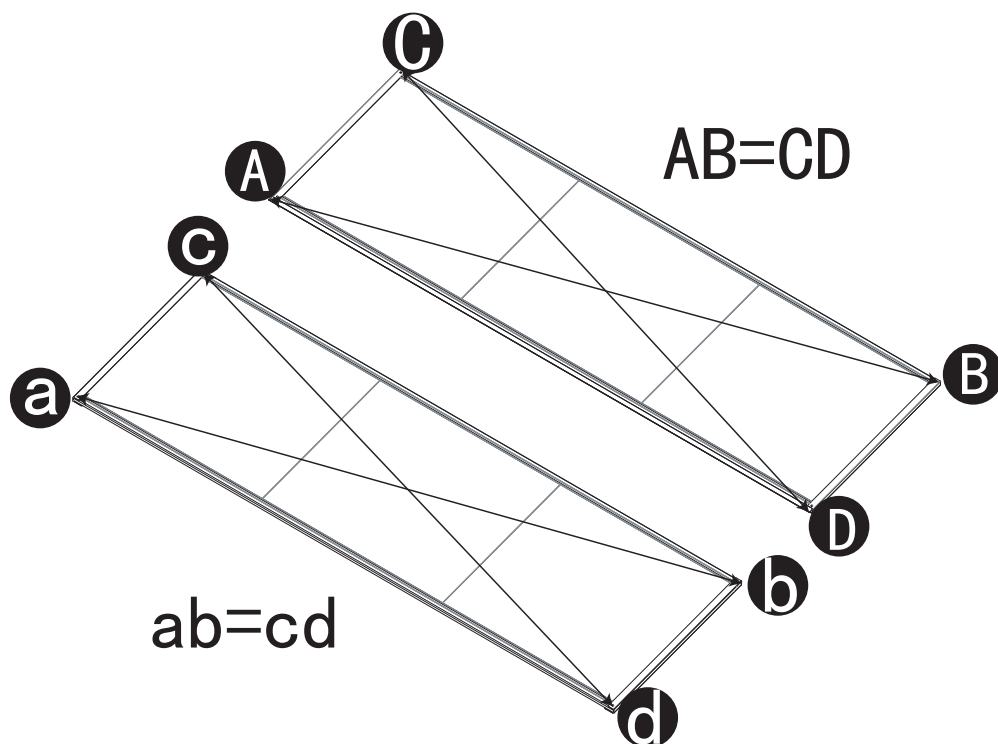
DA4

1 PC
DS1

1 PC

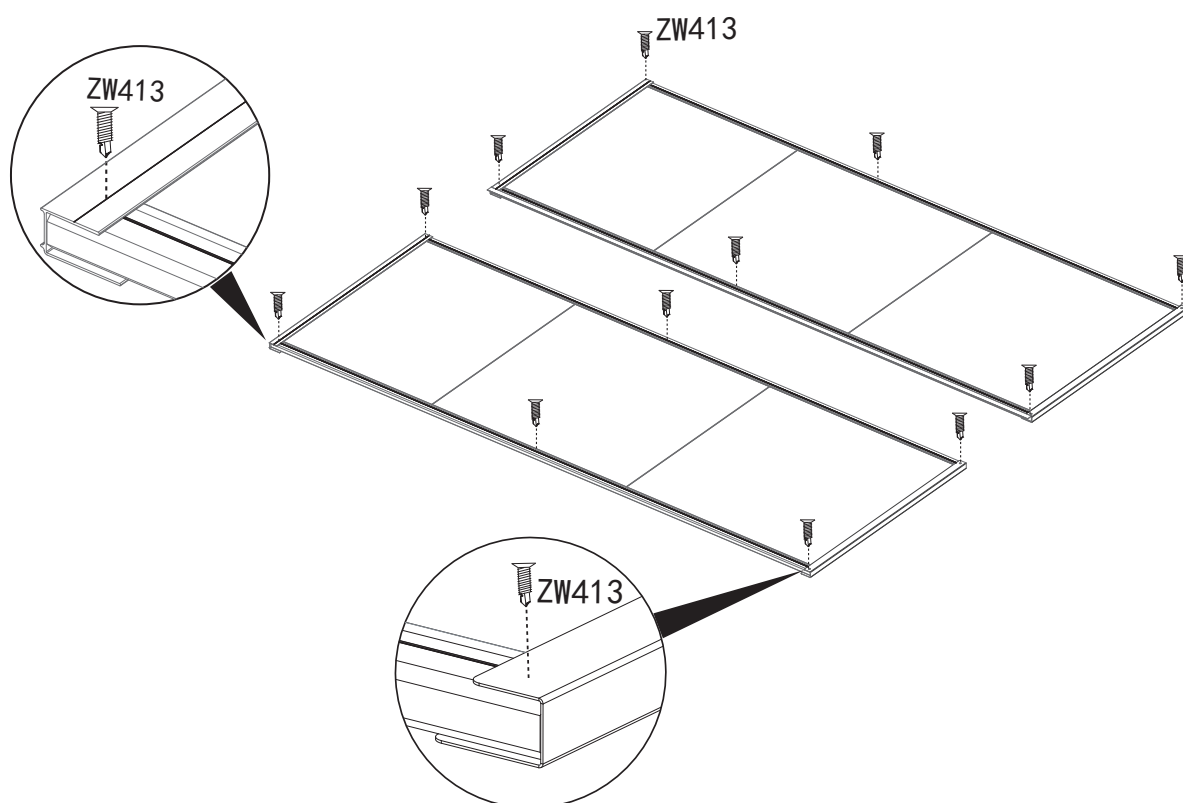
Step 36



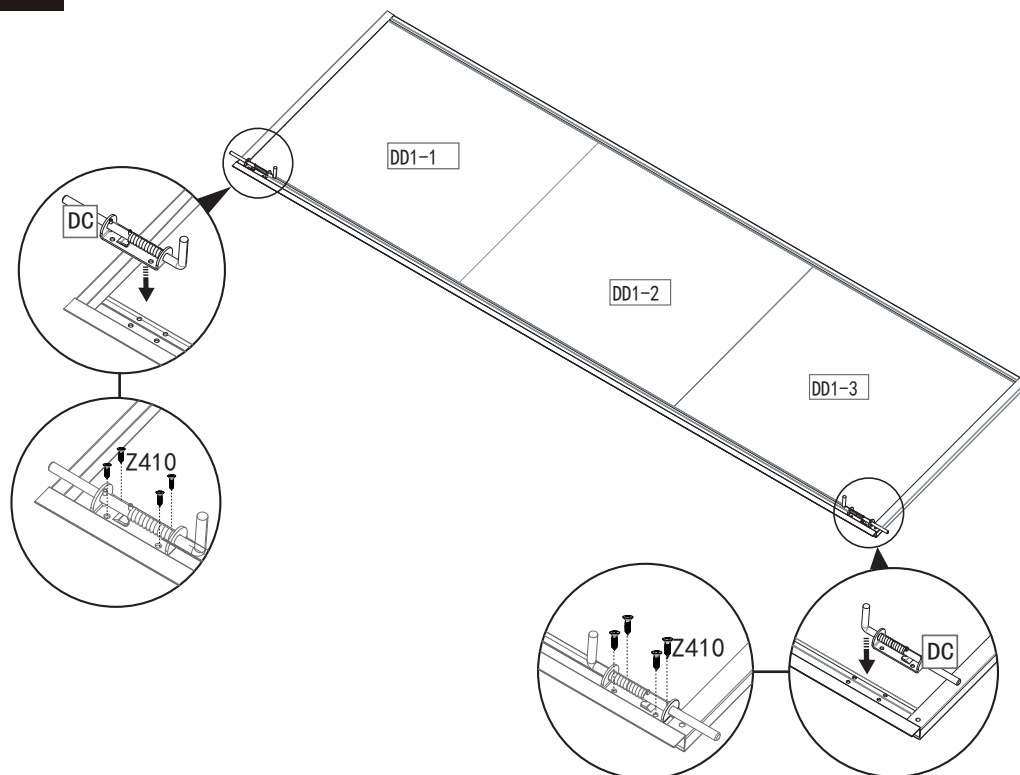
Step 37



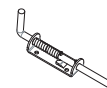
Step 38



Step 39



DC



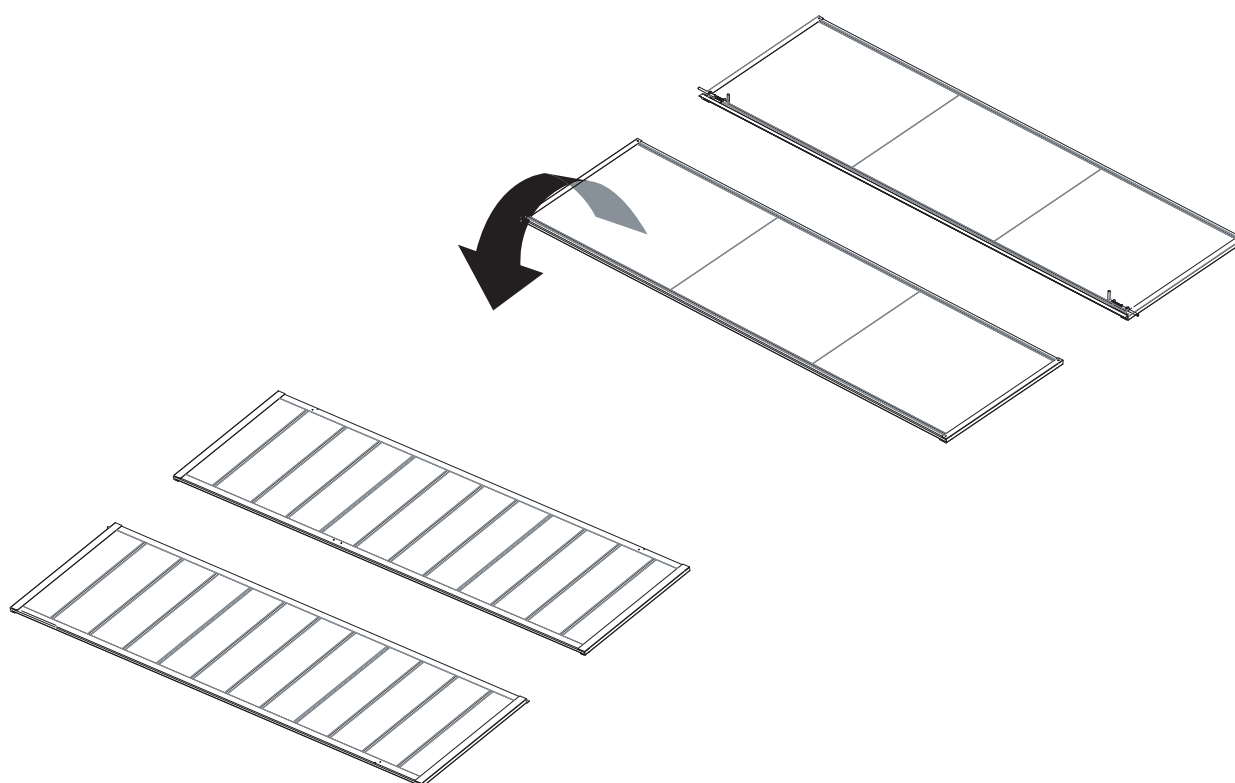
2 PC

Z410

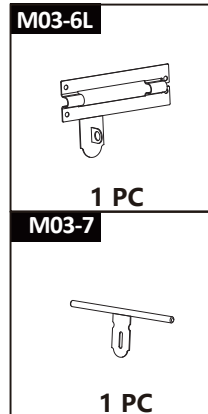
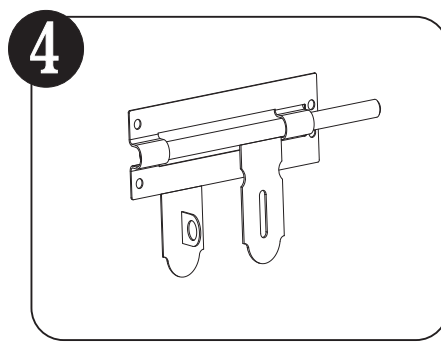
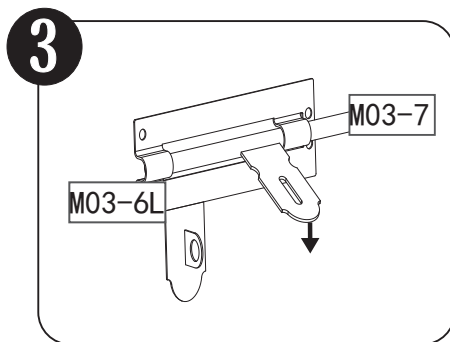
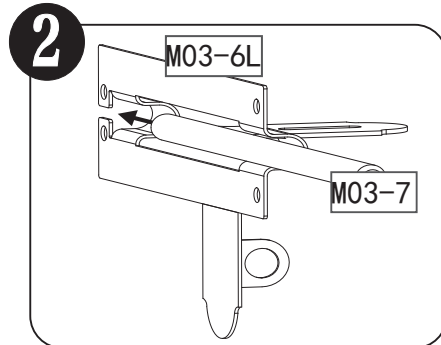
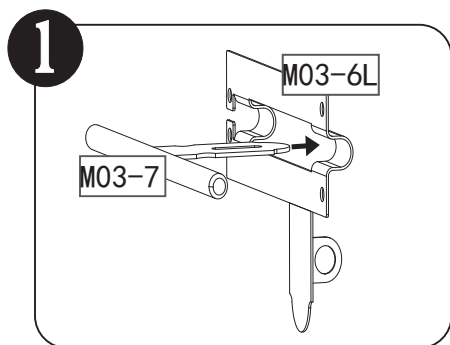


8 PC

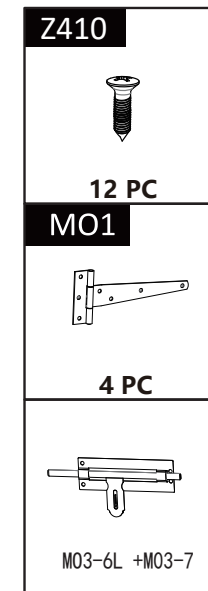
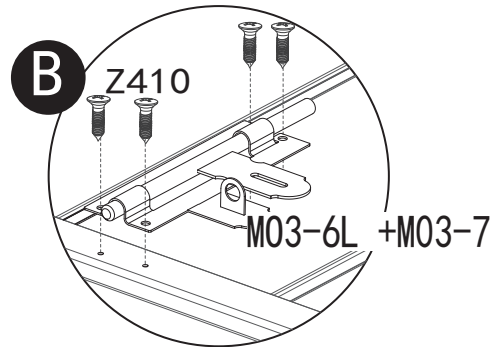
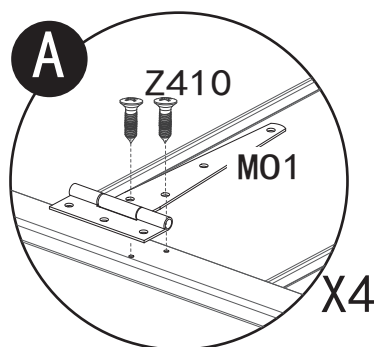
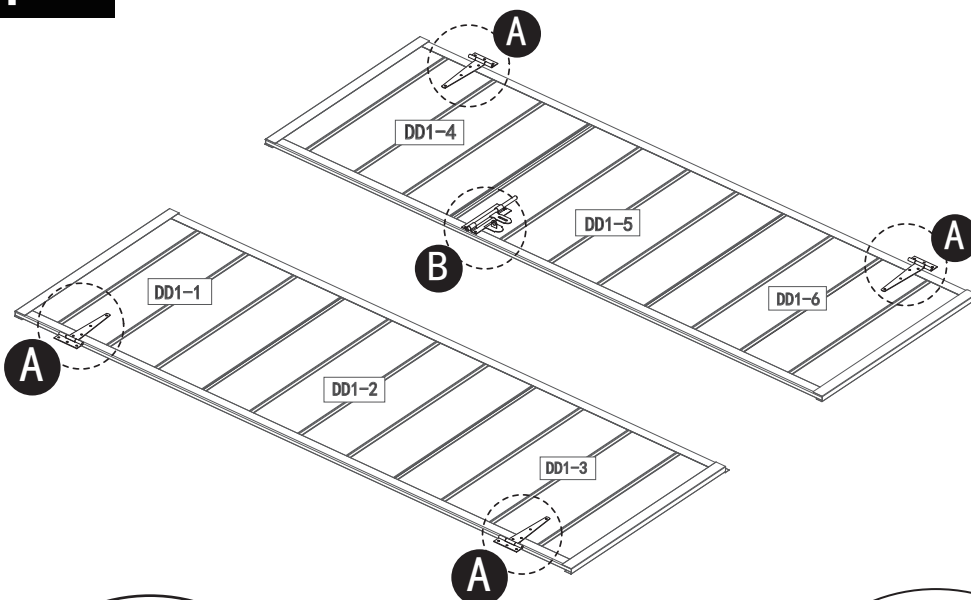
Step 40



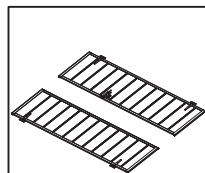
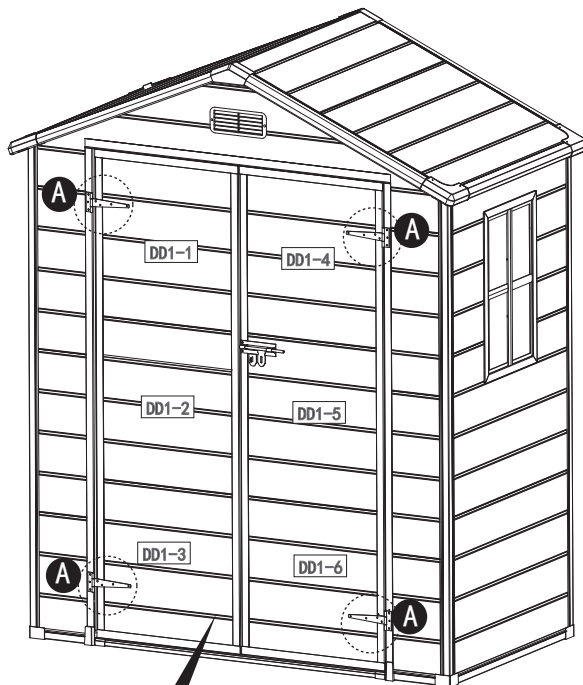
Step 41



Step 42



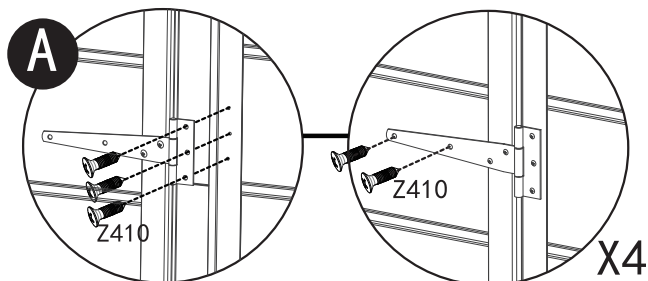
Step 43



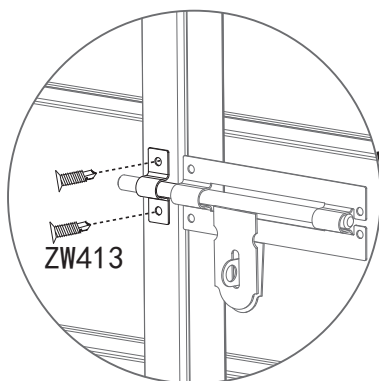
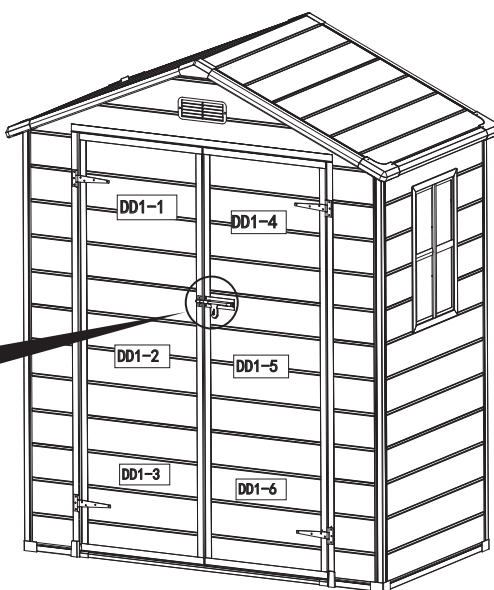
Z410



20 PC



Step 44



ZW413



2 PC

MO4



1 PC